AN INNOVATIVE WAY FOR COMPUTERIZED SMITH CHART GENERATION AND TRANSMISSION LINE PROBLEM SOLVING

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Abstract

The Smith chart is one of the most useful graphical tools for high frequency circuit applications. It provides a clever way to visualize complex functions and solving problems with transmission lines and matching circuits. Here a computer-assisted learning technique is presented using C language for making lively and dynamic problem solving interface, by which one can implement the smith chart and solve problems easily related to transmission line and matching circuit. The concept of explaining the use of an important old tool like the Smith Chart, using modern tool like C to explain the use of the Smith Chart represents a prime example of the melding of the traditional and the leading edge. Here, we have solved some of the most common transmission line problems using this software and have compared the results of both experimental calculations as well as manual calculations.

Keywords: Smith chart, Transmission line, Impedance matching Reflection co-efficient, VSWR, Normalized Impedance, Normalized admittance

1. INTRODUCTION

Smith chart is a graphical tool for solving transmission line problems which was developed by P. H. Smith [1]. It reveals graphically the complex impedance anywhere along a line. The smith chart is very useful when solving transmission problems. The real utility of the Smith chart, it can be used to convert from reflection coefficients to normalized impedances (or admittances), and vice versa [2]. The mathematics of transmission lines becomes cumbersome at times, especially when dealing with complex impedances and nonstandard situations. In 1939, Phillip H. Smith published a graphical device for solving these problems, the Smith Chart. It consists of a series of overlapping orthogonal circles that intersect each other at right angles. These sets of orthogonal circles make up the basic structure of the Smith chart and are shown in Fig. 1[3]. When illustrating fundamental concepts the Smith chart is the ideal tool, especially at the level where concepts are being explained to students and when they are exposed to these concepts for the first time. The Smith chart provides an extremely useful way of visualizing transmission line phenomenon, and so is also important for pedagogical reasons. Microwave and RF engineers can develop intuition about transmission line impedance matching problems by learning to think in terms of the Smith chart [4]. In the modern age, using the Smith chart to perform calculations has gone the way of the slide rule; such calculations are best done using a computer program. However, the utility of the Smith chart to enhance the understanding of complex impedances has not diminished. Indeed, use of the Smith chart has grown over the decades since its invention. The Smith chart remains one of the most widely used nomographs used in the practice of electrical engineering [5].

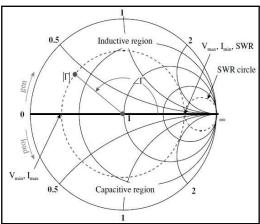


Fig-1: A Smith chart

2. TRANSMISSION LINE PARAMETERS

The following transmission line parameters are associated with this method-

2.1 Reflection Co-efficient

The reflection co-efficient is used in physics and electrical engineering when wave propagation in a medium containing discontinuity is considered. It is designated by $\Gamma(\text{gamma})$ and is defined as-

 Γ = reflected voltage or current/incident voltage or current

- $= V_{ref}/V_{inc}$
- $= I_{ref}/I_{inc}$

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2.2 Transmission Co-efficient

A transmission line terminated in its characteristic impedance Zo is called a properly terminated line. According to the principle of conservation of energy, the incident power minus the reflected power must be equal to the power transmitted to the load. This can be expressed as

$$1 - \Gamma_r^2 = (Z_0/Z_r).T^2$$

where T is the transmission coefficient and is defined as-

T=transmitted voltage or current / incident voltage or current $= \mathbf{V}_{tr} / \mathbf{V}_{inc}$ $= \quad \boldsymbol{I}_{tr} \, / \, \boldsymbol{I}_{inc}$

2.3 Voltage Standing Wave ratio

The ratio of the maximum of the voltage standing-wave pattern to the minimum is defined as the voltage standing wave ratio (ρ) and is given by-

$$\rho = VSWR = |Vmax| / |Vmin|$$

Also,
$$\rho = [(1 + |\Gamma|)/(1 - |\Gamma|)]$$

2.4 Normalized Impedance

The normalized impedance with characteristic impedance (Zo) may be calculated by dividing the load impedance (Z_I) by the characteristic impedance i.e. $z_L = Z_L / Z_0$

For a lossless transmission line-

The maximum normalized impedance, $Z_{max}=1+|\Gamma|/1-|\Gamma|$

2.5 Normalized Admittance

The normalized is the reciprocal of normalized impedance. i.e. $y = Z_0 / Z_L = 1/ z_L$

Actual admittance = y/Zo

2.5 Stub

Impedance matching can be achieved by inserting a section of short circuited transmission line (in shunt with the main line) in between the load and source is called stub. This process is called stub-matching.

3. MATHEMATICAL FORMULATION

The following formulas are used in implementation of the software [6]-

- (1) characteristic impedance $Z_0 = R_0 + i X_0$
- (2) load impedance $Z_L = R_L + i X_L$

(3)

$$\Gamma = \frac{Z_L - Z_0}{Z_L + Z_0} = \frac{z_L - 1}{z_L + 1}$$
 where normalized load impedance, $z_L = Z_L/Z_0$

- (4) $\Gamma = \Gamma r + j\Gamma_i = |\Gamma| e^{j\theta}$
- (5) normalized load impedance,

$$r_{L} + j x_{L} = \frac{1 + \Gamma_{r} + j\Gamma_{i}}{1 - \Gamma_{r} - j\Gamma_{i}}$$

$$= \frac{1 - \Gamma_{r}^{2} - \Gamma_{i}^{2}}{(1 - \Gamma_{r})^{2} + \Gamma_{i}^{2}}$$

$$+ j \frac{2\Gamma_{i}}{(1 - \Gamma_{r})^{2} + \Gamma_{i}^{2}}$$

where Γr , Γ_i are real and imaginary part of reflection coefficient respectively and r_L , x_L are real and imaginary part of normalized impedance respectively

(6) Equations for drawing constant resistance and constant reactance circle-

$$\left(\Gamma_r - \frac{r_L}{r_L + 1}\right)^2 + (\Gamma_i - 0)^2 = \left(\frac{1}{r_L + 1}\right)^2$$
$$(\Gamma_r - 1)^2 + \left(\Gamma_i - \frac{1}{x_L}\right)^2 = \left(\frac{1}{x_L}\right)^2$$

4. SOFTWARE IMPLEMENTATION STEPS

4.1 Preparation of Smith Chart

- A circle is drawn with a horizontal line passing through its centre
- The circles of constant resistance and conductance are drawn
- The lines of constant reactance and susceptance are
- Two more outer circles (inner wavescale and outer wavescale) are drawn and marked

4.2 To Determine Reflection Co-efficient

- The input values are load resistance, load reactance and characteristic impedance
- After that the normalized resistance and normalized reactance are calculated
- Then the constant resistance and constant reactance circles are drawn
- The magnitude of reflection coefficient is calculated from the origin to the point of intersection of the two circles
- The angle is calculated from the slope of the line

4.3 To Determine VSWR

- Repeat the steps of determination of reflection coefficient as given in (4.2)
- Taking the reflection coefficient as the radius, the VSWR circle is drawn with origin at the centre of the screen
- Wherever the circle cuts the horizontal line in the right hand side, it is the location of VSWR and in the left hand side, it is 1/ VSWR

4.4 To calculate Admittance Point

Repeat the steps of calculation of VSWR as given in (4.3)

- The line drawn from the origin to the point of intersection of the two circles to find magnitude of reflection coefficient is extended in opposite direction and finds the point where it cuts the VSWR circle
- Then the normalized conductance and normalized susceptance is calculated. This gives the normalized admittance point

4.5 To Calculate Zmin and Zmax

- Repeat the steps of determination of reflection coefficient as described in (4.2)
- Taking the reflection coefficient as the radius, the VSWR circle is drawn with origin at the centre of the screen
- Wherever the circle cuts the horizontal line in the right hand side, it is the location of Zmax and in the left hand side, it is Zmin

4.6 To Calculate Voltage Minimum (Vmin)

- Repeat the steps of determination of reflection coefficient as described in (4.2)
- The location of the reflection co-efficient point is found from the wavescale and deducted from the total wavescale value i.e. 0.5λ . This gives the location of voltage minimum

4.7 To Calculate Input Impedance and Admittance

- Repeat the steps of determination of reflection coefficient as described in (4.2)
- The reflection coefficient as the radius, the VSWR circle is drawn with origin at the centre of the screen
- The location of the above point is found from the wavescale and the given distance is added to this value and new location is found. Now a line is drawn from this point to the centre of the VSWR circle. The point of intersection of the line with VSWR circle is determined. Now, resistance and reactance of the point are found. This gives the input impedance
- Similarly input admittance is also determined

4.8 To Calculate Location of Stub

- The location of the admittance point is found as done in step 4.4
- The location of the admittance point is found from the wavescale. This gives the location of the load
- The point at which VSWR circle cuts the unit conductance circle is determined. The location of the above point is found from the wavescale. This gives the location of the stub

4.9 To Calculate Location at which Real Part of the Admittance Equals the Line Characteristic Impedance

- The input admittance point is determined as mentioned above. Location of this point is found from wavescale
- The point at which VSWR circle cuts the unit conductance circle is determined. The location of the above point is found from the wavescale
- The difference between the abobe two points gives the location nearest to the load at which the real part of the admittance equals the line characteristic admittance

4.10 To Continue or Exit

• The program asks the user if he wants to continue with with the program or not by choosing the option y (to continue) and n (to exit)

5. SOFTWARE APPLICATIONS

The implemented software can be used in the following cases-

- Determination of reflection co efficient from a given load impedance
- Determination of VSWR from a given load impedance
- Determination of load admittance from a given load impedance
- Determination of Zmax and Zmin (maximum impedance and minimum impedance respectively)
- Determination of distance of first voltage minimum of the standing wave pattern from a given load impedance
- Determination of input impedance and input admittance from a given load impedance at a given distance
- Determination of location of the point at which real part of the line admittance is equal to the line characteristic impedance from a given load impedance
- Determination of location of load and stub from a given load impedance and operating frequency

6. EXPERIMENTAL RESULTS

In this column various transmission line related problem solving results (found by both experimentally and manually) along with software outputs are listed in table (1-8) and figure (2-9).

Table -1

Determination of Reflection Co-efficient	
Given Data	load Impedance, Z= $(27.5+j80) \Omega$
	Characteristic impedance, Zo= 50Ω
Experimental	
results	$0.746 < 59.8^{\circ}$
Manually	
calculated	$0.75 < 60^{0}$
value	
Figure No	Fig 2

Table -2

Determination of VSWR	
Given Data	Load Impedance, Z= (50- j 100) Ω Characteristic impedance, Zo= 75 Ω
Experimental results of the software	4.616
Manually calculated value	4.6
Figure No	Fig 3

Table -3

Determination of Load admittance	
Given Data	load Impedance, Z= $(50+j\ 100)\ \Omega$ characteristic impedance, Zo= $50\ \Omega$
Experimental results of the software	normalized admittance = (0.2-j 0.4) actual admittance = (0.004- j 0.008) mho
Manually calculated value	normalized admittance =(0.2-j 0.4) actual admittance=(0.004- j0.008) mho
Figure No	Fig 4

Table -4

Determination of maximum and minimum impedance (Zmax and Zmin)	
Given Data	load Impedance, Z= (50- j 100) Ω
	characteristic impedance, Zo= 75 Ω
Experimental	Zmax = 4.61
results of the	Zmin = 0.216
software	
Manually	Zmax = 4.6
calculated value	Zmin = 0.22
Figure No	Fig 5

Table -5

standing wave pattern from a given load impedance	
Given Data	load Impedance, $Z=(150-j\ 20)\ \Omega$
	characteristic impedance, Zo= 50Ω
Experimental	distance of first voltage minimum =
results of the	0.065 λ
software	
Manually	distance of first voltage minimum =
calculated value	0.065 λ
Figure No	Fig 6

Table -6

Determination of input impedancee and input admittance	
from a given load impedance at a given distance	
Given Data	load Impedance, Z= (150- j 20) Ω
	characteristic impedance, Zo= 50Ω
	given distance = 0.05λ
Experimental	normalized impedance = (0.257-j 0.08)
results of the	normalized admittance = $(3.4 + j 1.19)$
software	
	actual impedance = $(12.89 - j 4.44) \lambda$
	actual admittance = $(0.069 + j \ 0.023)$
	mho
Manually	normalized impedance = $(0.22-j\ 0.1)$
calculated value	normalized admittance = $(3.5 + j 1.1)$
	actual impedance = $(13.5 - j 5) \lambda$
	actual admittance = $(0.065 + j \ 0.022)$
	mho
Figure No	Fig 7

Table -7

Determination of location of the point at which real part of line admittance is equal to the line characteristic impedance from a given load impedance	
Given Data	load Impedance, Z= (15- j 20) Ω characteristic impedance, Zo= 50 Ω
Experimental	0.1396 λ
results of the	
software	
Manually	0.14 λ
calculated value	

Table -8 Determination of location of load and stub and also the

Fig 8

Figure No

input susceptance of the stub from a given load impedance and operating frequency	
Given Data	load Impedance, Z= $(30-j 40) \Omega$
	characteristic impedance, Zo= 50Ω
Experimental	location of the load = 0.1249λ
results of the	location of the stub = 0.166λ
software	location of the stub from load= 0.0083 m
	input susceptance of the stub= -0.0230
	mho
Manually	location of the load = 0.125λ
calculated	location of the stub = 0.165λ
value	location of the stub from load = 0.008 m
	input susceptance of the stub= -0.0236
	mho
Figure No	Fig 9

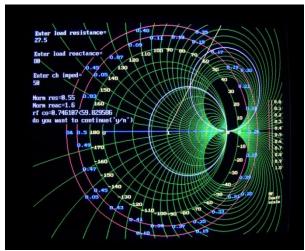


Fig -2: Software output for determining reflection coefficient

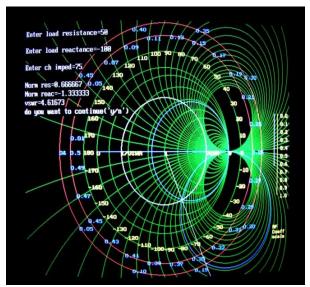


Fig -3: Software output for determining VSWR

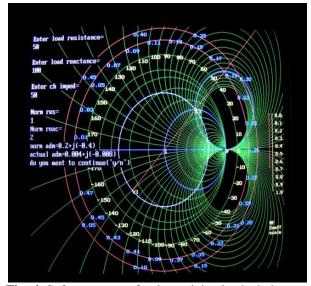


Fig -4: Software output for determining load admittance

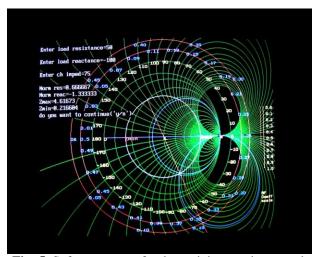


Fig -5: Software output for determining maximum and minimum impedance

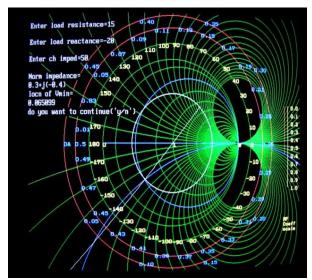


Fig -6: Software output for determining distance of first voltage minimum from a given load impedance

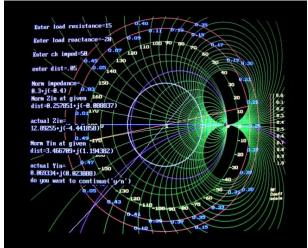


Fig -7: Software output for determining input impedance and input admittance from a given load impedance at a given distance

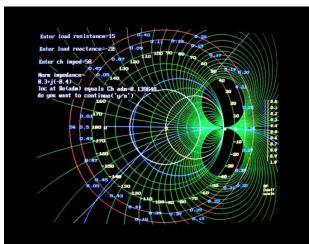


Fig -8: Software output for determining location of the point at which real part of line admittance is equal to the line characteristic impedance from a given load impedance

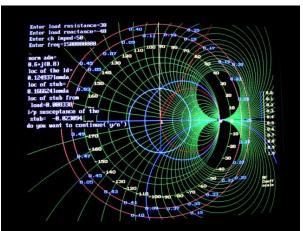


Fig -9: Software output for determining location of load and stub and also the input susceptance of the stub from a given load impedance and operating frequency

7. CONCLUSIONS

The study of transmission lines is basic in undergraduate electrical and electronics engineering education. In relevant courses, a key subject is the Smith chart, which is a graphical device providing insight [7]. Understanding the Smith chart is still important nowadays, despite the present generalization of personal computers and powerful calculators. It is easy to plug a few numbers into a program and have it spit out solutions. When the solutions are complex and multifaceted, having a computer to do the grunt work is especially handy. However, knowing the underlying theory and principles that have been ported to computer platforms, and where they came from, makes the engineer or designer a more well-rounded and confident professional, and makes the results more reliable. Moreover it is interesting to note that these kinds of graphical tools are still useful now adays [8]. This software provides a powerful computation and display platform by means of which fundamental and advanced concepts relating to transmission lines can be understood and implemented [9]. This software is very simple for beginners, cost effective, reliable and easy

to handle. It also requires very less time to execute. Furthermore, the computations are more accurate and the results can be displayed and interpreted in a more effective manner.

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