

ACTIVE POWER AND CURRENT CONTROL FOR A MODULAR MULTILEVEL CONVERTER (MMC) BASED SYSTEM

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Abstract

Modular Multilevel Converter (MMC) has found very wide application in renewable energy sources, DC power transmission, high voltage applications and micro grid. However the technical challenge of MMC is to maintain constant current, constant power and constant voltage at the output depending on the requirement. In this paper a control loop is developed for maintaining constant power at the MMC. The power controller is realized by using PI controller. The PWM technique used for generation of gating pulses is Phase Disposition PWM (PD-PWM) technique, The simulation of current and the active power controller is carried out by MATLAB/SIMULINK and the results are presented.

Keywords: Modular Multilevel Converter (MMC), Phase Disposition PWM (PD-PWM) technique, Proportional Integral (PI) controller, Current controller, Sub Module (SM), and Active power controller.

1. INTRODUCTION

The MMC has found very wide application in High Voltage DC (HVDC) power transmission, renewable energy sources, and micro grids. The other well known topology is multilevel topology, But it as few disadvantages such as it can be used only in medium voltage application, The number of components required increases as the voltage level increases and number of the circuit components increase proportional to the power capacity. Compared to multilevel converter MMC has few advantages such as:

- Harmonic distortion is found to be very less, due to increased number of voltage levels compared to that of a multilevel converter.
- MMC circuit configuration is very simple compared to multilevel converter.
- Even when few of the sub modules are out of order, the MMC can remain in operating state.
- DC link current and arm current is found to be continuous in MMC, hence the DC link capacitor can be omitted [1].

2. MODULAR MULTILEVEL CONVERTER (MMC)

Fig-1, shows the MMC circuit that is considered for simulation studies in this paper. It consist of six valves each connecting one AC terminal and one DC terminal. Each valve consists of a number of sub modules and each sub module is a two level converter consisting of IGBTs connected in series across the capacitor, with the midpoint connection and one of the two capacitor terminals brought out as external connections. The sub module can be either half bridge or full bridge configuration. With a suitable number of sub modules connected in series, the valve can synthesize a stepped voltage waveform that approximates

very closely to a sine-wave and contains very low level of harmonic distortion. The MMC differs from other types of converter in that, current flows continuously in all six valves of the converter throughout the cycle. The direct current splits equally into the three phases and the alternating current splits equally into the upper and lower valve of each phase. The current in each valve is therefore, related to the direct current I_d and alternating current I_{ac} as follows [2].

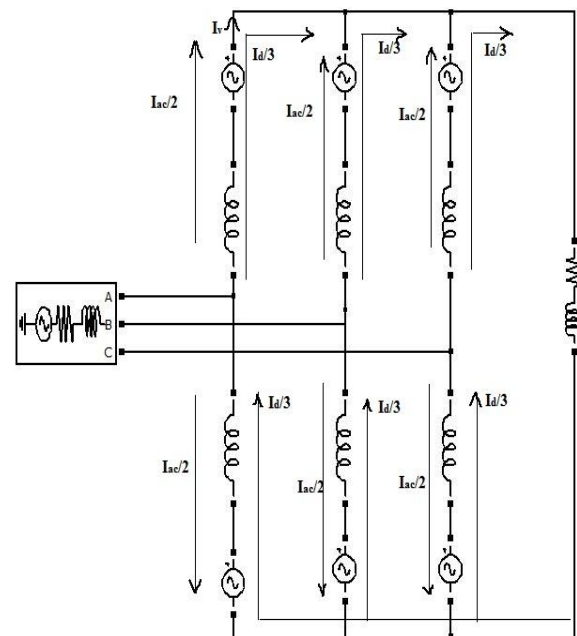


Fig-1: Three-phase MMC.

Upper valve

$$I_v = I_d/3 + I_{ac}/2 \quad (1)$$

Lower valve

$$I_v = I_d / 3 - I_{ac} / 2 \tag{2}$$

Where, I_v =valve current
 I_d =direct current
 I_{ac} =alternating current

PI based controllers are developed for constant DC voltage as well as constant DC power for load variation. In this regard a PI controller based power controller for MMC is developed in this paper. The overall control scheme also involves a current controller, which gets the input signal from the power controller [3].

In this paper, half bridge configuration of sub module is used to build MMC for controlling the current and active power.

3. GATE PULSE GENERATION FOR MMC USING PD-PWM METHOD

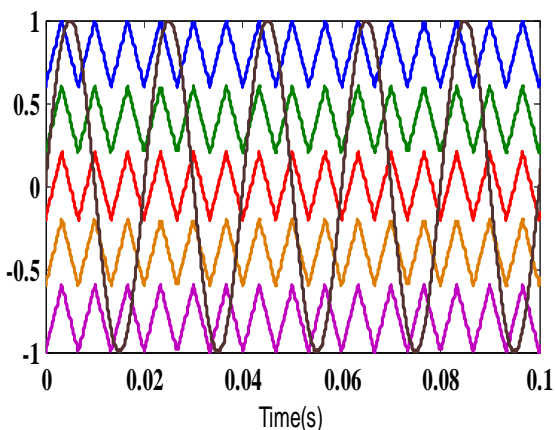


Fig-2: PD-PWM method waveform for 6level MMC.

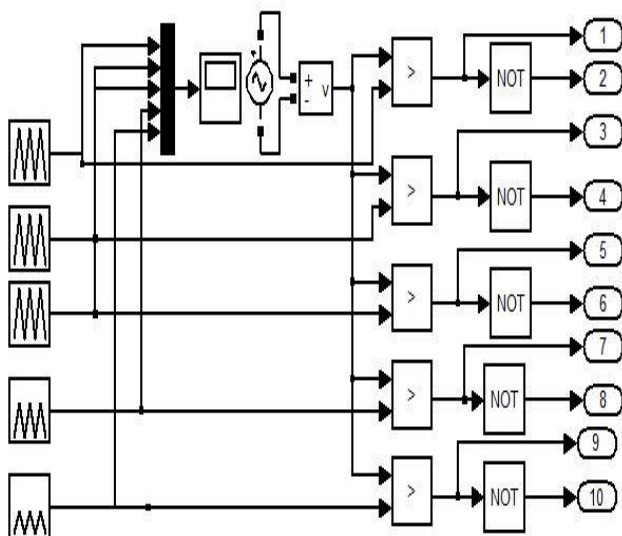


Fig-3: Simulation model of PD-PWM method for 6 level MMC.

In PD-PWM method all carrier waveforms are in phase. As shown in Fig-2, here a-phase modulation signal is compared with N-1 triangle waveforms, where N is the number of levels Here Fig-3, shows the simulation model implemented for 6 level MMC [4].

4. ACTIVE POWER AND CURRENT CONTROLLER

The aim of the active power controller is to maintain a constant active power at the reference value. The block diagram of active power controller is shown in Fig-4, where the current controller is in the inner loop of active power controller [5].

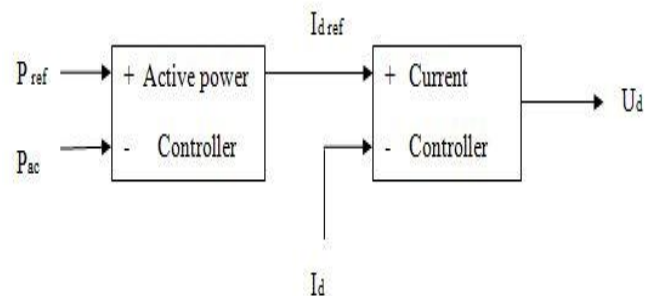


Fig-4: Block diagram of active power controller.

Fig-5, shows the active power controller where the reference active power P_{ref} is fixed, the measured power P_{ac} is compared with P_{ref} , difference is given to a PI controller which controls the active power.

The Fig-6, shows the d component of current controller here the current is controlled by using a PI controller, the output of the PI controller is summed with the voltages as shown in Fig-5, thus the modulating reference voltage of d component is obtained [1].

Fig-7, shows the q component of current controller, where the q component of current is controlled similar to that of the d component.

The current controller is placed inside the power controller loop for the control of active power.

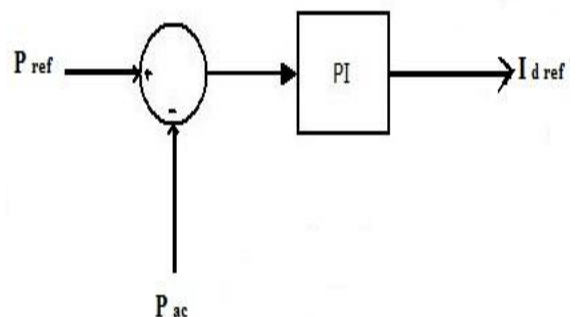


Fig-5: Active power controller.

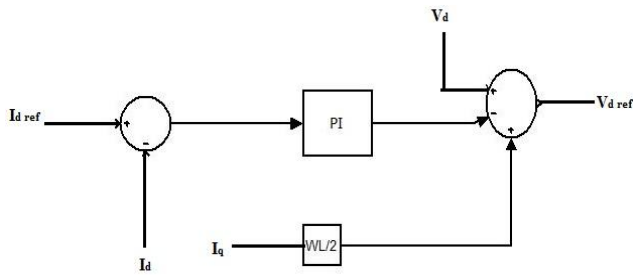


Fig-6: Id current controller.

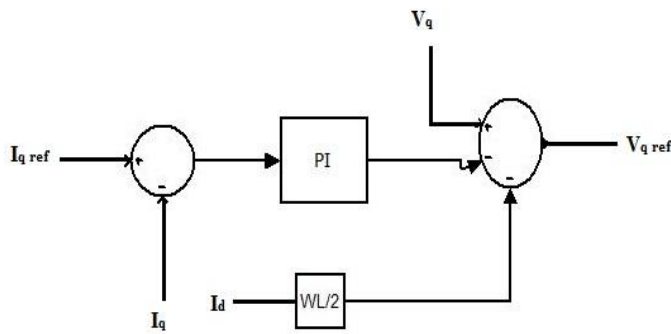


Fig-7: Iq current controller.

As seen from the Fig-5, Fig-6 and Fig-7, PI controller plays a vital role for the control of active power and current. Hence it is necessary to find accurate values of K_p and K_i for the active power and the current controller. The rating of the system is chosen and the set of equations used for finding the initial values of K_p and K_i are given below:

AC supply voltage : 3000kV (L-L rms)

$P_{dc}=10kw$

$$P=3/2 * V_b I_b \tag{3}$$

$$V_{ac}=m V_{dc}/2 \tag{4}$$

V_{ac} is the peak phase voltage

$$P_{dc}=3/2 * V_b I_b = V_{dc} I_{dc} \tag{5}$$

$$Z_{base}=V_b/I_b \ \Omega \tag{6}$$

$$L_{base}=Z_b/W_b \ H \tag{7}$$

$w_b=w_{act}=314$ radians

$$R_{pu}=R_{act}/R_{base} \tag{8}$$

$$Z_{base}=R_{base} \tag{9}$$

$$K_p=T_i * R_{pu}/2T_a$$

$$K_i=K_p/T_i \tag{10}$$

$K_p=0.524$

$K_i=0.1483$

Where, P_{dc} =dc power output

V_{dc} =dc voltage

P =ac power input

- V_b =base voltage
- I_b =base current
- m =modulation index
- Z_{base} =base impedance
- R_{base} =base resistance
- L_{base} =base inductance
- R_{pu} =resistance in pu
- T_i =integral time constant
- T_a =average time
- K_p =proportional gain
- K_i =integral gain

The initial values of K_p and K_i are adjusted to get a good response.

5. SIMULATION AND RESULTS

The Fig-8, shows the simulation model of a MMC with current and active power controller.

Fig-9, and Fig-10, shows the performance of the current controller under steady state condition. Fig. 9, is the controlled I_d current waveform for a reference current of 1pu. Fig-10, is the controller I_q current waveform for a reference current of 0pu. Fig-11, shows the performance of the current controller under dynamic condition for a step change in the reference I_d current, the controller is able to track the step change in the reference value with a time delay of 0.2msec. Fig-12 and Fig-13, shows the d and q component of modulation index of the reference (modulating) voltage waveform.

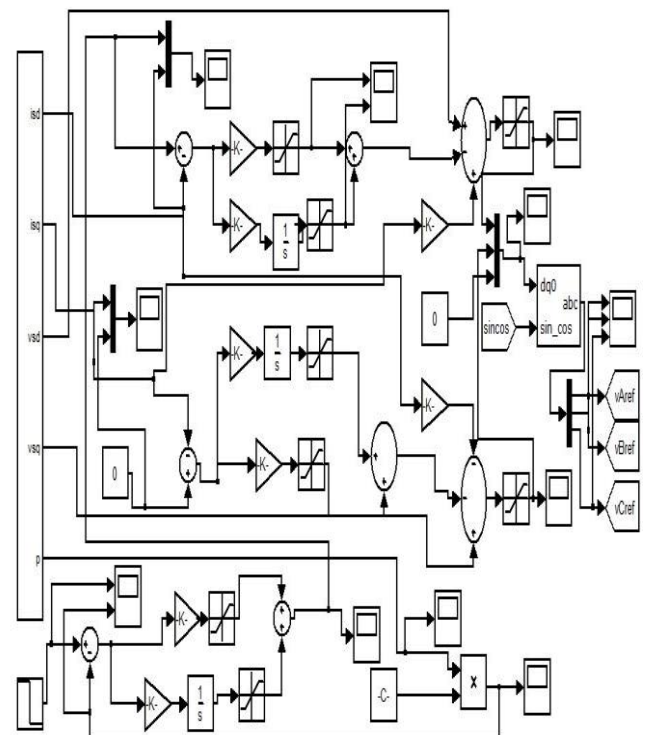


Fig-8: Simulation model of active power and current controller.

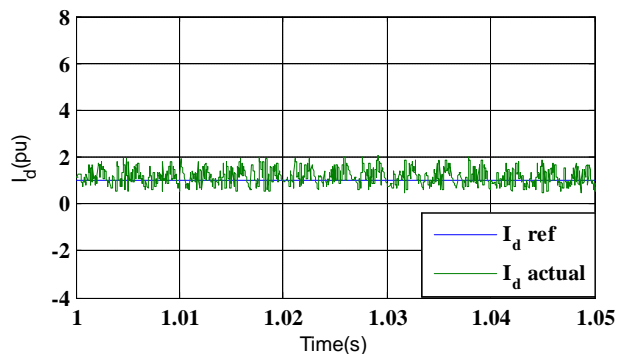


Fig- 9: Controlled I_d current waveform.

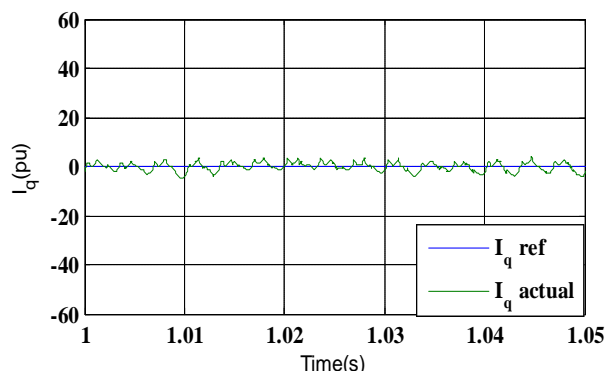


Fig-10: Controlled I_q current waveform.

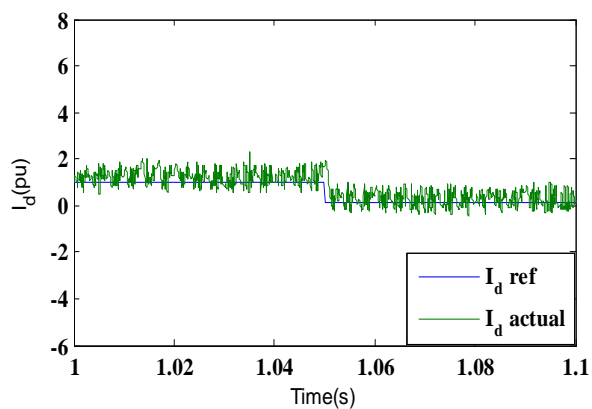


Fig-11: Controlled I_d current waveform under dynamic condition.

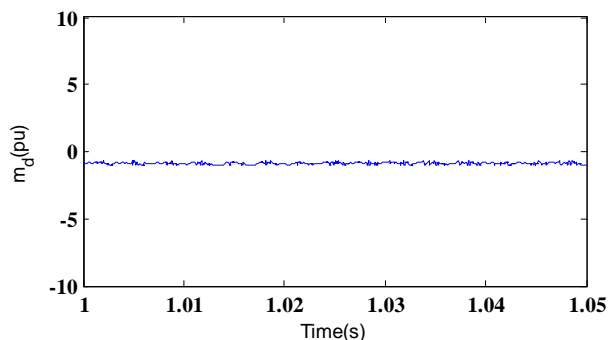


Fig-12: Modulation index of d component waveform

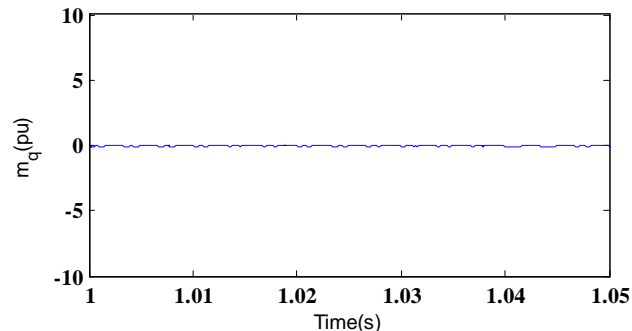


Fig-13: Modulation index of q component waveform.

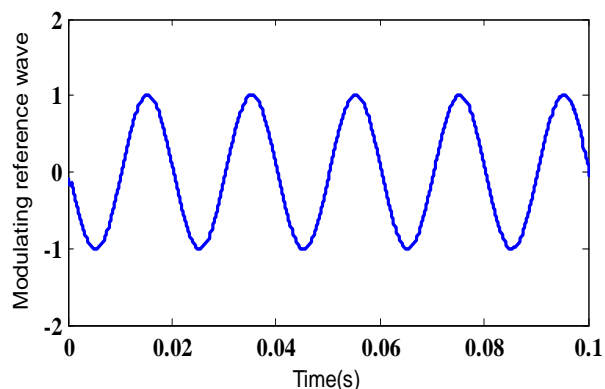


Fig-14: Modulating reference voltage waveform.

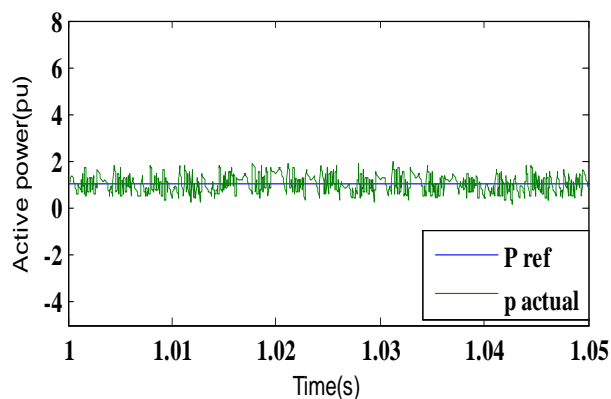


Fig-15: Controlled active power waveform.

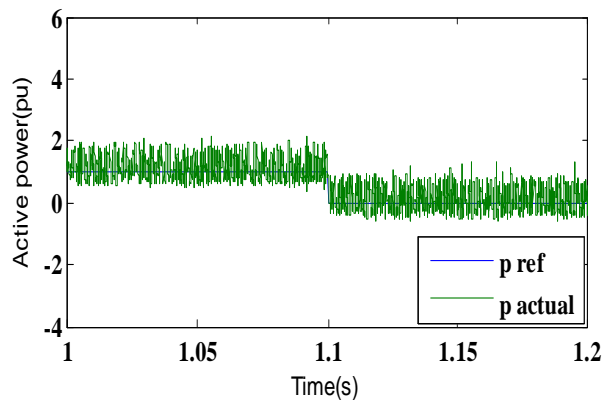


Fig-16: Controlled active power waveform under dynamic condition.

Fig-14, shows the modulating reference voltage waveform generated. Fig-15, shows the performance of the active power controller waveform for a reference active power of 1pu under steady state condition and the Fig-16, shows the performance of the active power controller under dynamic condition, that is for a step change in the reference active power the controller is able to track the step change in the reference value with a time delay of 0.2msec.

6. CONCLUSIONS

In this work, the gating pulse generation for a MMC using PD-PWM technique is developed and the output of the MMC is observed. Controllers are developed by using PI controller for control of current and active power, the performance of the controller is verified under steady state and under dynamic condition, the controller is found to be working good and it is able to track the step change in the reference value with a time delay of 0.2msec. The work can be further extended to implement reactive power controller.

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BIOGRAPHIES



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