

STATE SPACE VECTOR BASED ADVANCED DIRECT POWER CONTROL OF MATRIX CONVERTER AS UPFC

P. Lakshmi Bhargavi¹, S. Suresh Reddy², S. Sarat Kumar Sahu³

¹PGScholar, Dept of PS (EEE), ²Head of the Dept of EEE, ³Professor, Dept of EEE, NBKR, Vidyanagar, APMVGR, Vizayanagaram, AP, padarthi224@gmail.com, sanna_suresh@reddifmail.com, sahu.sarat@gmail.com

Abstract

This paper presents state space vector analysis for three-phase matrix converters operating as unified power flow controllers (UPFCs). It allows direct ac/ac power conversion without dc energy storage links; therefore, It reduces volume, cost, capacitor power losses, together with higher reliability. The line active and reactive power, together with ac supply reactive power, can be directly controlled by selecting an appropriate matrix converter switching state guaranteeing good steady-state and dynamic responses. This advanced control of MC guarantee faster responses without overshoot and no steady-state error, presenting no cross-coupling in dynamic and steady-state responses. Simulations are carried out, showing the effectiveness of the proposed method in steady-state and transient conditions.

Keywords: Direct power control (DPC), matrix converter (MC), unified power flow controller (UPFC), state space vectors.

1. INTRODUCTION

In the last few years Flexible AC Transmission Systems (FACTS) became well known powerelectronics-based equipment to control transmission lines power flow. UPFCs are the most versatile and complex FACTS allowing precise and reliable control of both active and reactive power flow over the network. UPFC can prevail over line impedance dependencies, sending and receiving end voltage amplitudes and phase differences. The original UPFC concept, introduced in the nineties by L. Gyugyi[1], consists of two AC-DC converters using Gate-Turn Off thyristors (GTO), back to back connected through their common DC link using large high-voltage DC storage capacitors. Both converters AC sides are connected to the transmission line, through coupling transformers, in shunt and series connection with the line.

This arrangement can be operated as an ideal reversible AC-AC switching power converter, in which the power can flow in either direction between the AC terminals of the two converters. The DC link capacitors provide some energy storage capability to the back to back converters that help the power flow control. Replacing the two three-phase inverters by one matrix converter the DC link (bulk) capacitors are eliminated, reducing costs, size, maintenance, increasing reliability and lifetime. The AC-AC matrix converter, also known as all silicon converters, processes the energy directly without large energy storage needs. This leads to an increase of the matrix converter control complexity. In [2] an UPFC-connected power transmission network model was proposed with matrix converters and in [3] was used to synthesize both active (P) and reactive (Q) power controllers using a modified Venturini

high-frequency PWM modulator. In this paper a Matrix Converter based UPFC-connected power transmission network model is proposed, using a Direct Power Control approach (DPC-MC). This control method is based on sliding mode control techniques [5] and allows real time selection of adequate state-space vectors to control input and output variables. Transmission line active and reactive power flow can be directly controlled using this approach and the dynamic and steady state behaviour of the proposed P,Q control method is evaluated and discussed using detailed simulations. Results shows decoupled active and reactive power control, zero error tracking and fast response times.

2. MODELLING OF THE UPFC POWER SYSTEM

General Architecture

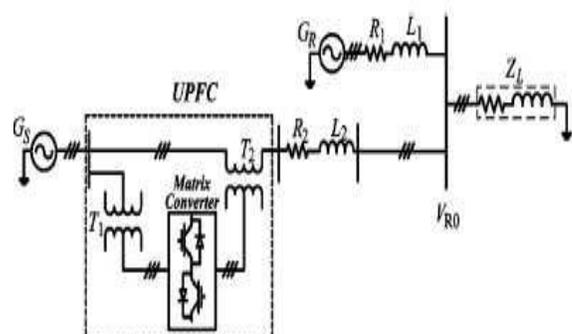


Fig.1. Transmission network with matrix converter UPFC

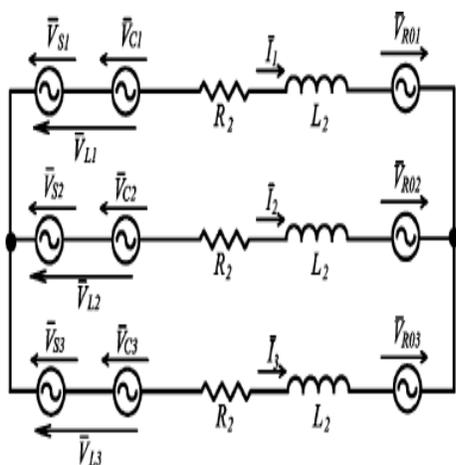


Fig.2. Three phase equivalent circuit of the matrix UPFC and transmission line

matrix converter is considered ideal and represented as a controllable voltage source, with amplitude V_c and phase ρ . In the equivalent circuit, V_{Ro} is the load bus voltage. The DPC-MC controller will treat the simplified elements as disturbances.

Considering a symmetrical and balanced three-phase system and applying Kirchhoff laws (Fig.2.), the ac line currents are obtained in dq coordinates as (1) and (2) equations.

$$\frac{dI_d}{dt} = \omega I_q - \frac{R_2}{L_2} I_d + \frac{1}{L_2} (V_{Ld} - V_{Rod}) \dots\dots(1)$$

$$\frac{dI_q}{dt} = -\omega I_d - \frac{R_2}{L_2} I_q + \frac{1}{L_2} (V_{Lq} - V_{Roq}) \dots\dots(2)$$

The active and reactive power of sending end generator are given in dq coordinates

$$\begin{bmatrix} P \\ Q \end{bmatrix} = \begin{bmatrix} V_d & V_q \\ V_q & -V_d \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix} \dots\dots(3)$$

Assuming V_{Rod} and $V_{Sd} = V_d$ as constants and a rotating reference frame synchronised to the V_s source so that $V_{Sq} = 0$, active and reactive power P and Q are given by (4) and (5), respectively.

$$P = V_d I_d \dots\dots(4)$$

$$Q = -V_d I_q \dots\dots(5)$$

Based on the desired active and reactive power (P_{ref} , Q_{ref}), reference current (I_{dref} , I_{qref}) can be calculated from (4) and (5) for current controllers. However, allowing P, Q actual powers are sensitive to errors in the V_d, V_q values.

B. Matrix converter output voltage and input current vectors

(Fig.3) includes the three-phase shunt input transformer (with windings T_a, T_b, T_c), the three-phase series output transformer (with windings T_A, T_B, T_C), and the three-phase matrix converter, represented as an array of nine bidirectional switches S_{kj} with turn-on and turn-off capability, allowing the connection of each one of three output phases directly to any one of the three input phases. The three-phase (ICr) input filter is required to re-establish a voltage-source boundary to the matrix converter, enabling smooth input currents.

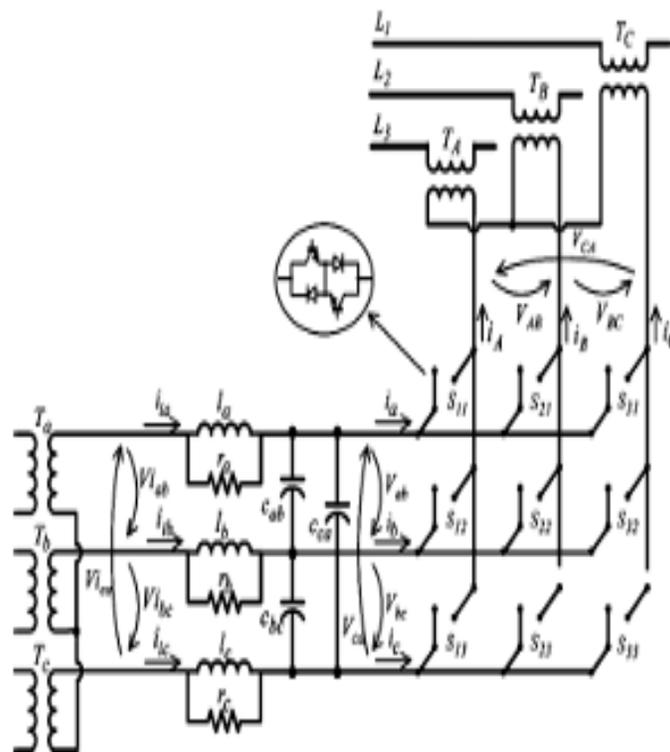


Fig.3. Transmission network with matrix converter UPFC

Applying dq coordinates to the input filter state variables and neglecting the effects of the damping resistors the equations obtained are:

$$\frac{di_{id}}{dt} = \omega i_{iq} - \frac{1}{2l} V_d - \frac{1}{2\sqrt{3}l} V_q + \frac{1}{l} V_{id}$$

$$\frac{di_{iq}}{dt} = -\omega i_{id} - \frac{1}{2l} V_q + \frac{1}{2\sqrt{3}l} V_d + \frac{1}{l} V_{iq}$$

$$\frac{dV_d}{dt} = \omega V_q + \frac{1}{2C} i_{id} - \frac{1}{2\sqrt{3}C} i_{iq} + \frac{1}{2\sqrt{3}C} i_q$$

$$\frac{dV_q}{dt} = -\omega V_d + \frac{1}{2C} i_{iq} + \frac{1}{2\sqrt{3}C} i_{id}$$

$$\frac{1}{2\sqrt{3}C} i_d - \frac{1}{2C} i_q \quad (6)$$

Where $V_{id}, V_{iq}, i_{id}, i_{iq}$ represents input Voltages and currents in dq components V_d, V_q, i_d, i_q the matrix converter voltages and input current in dq components, respectively.

Assuming ideal semi conductors, each matrix converter bi directional switch Sk_j can assume two possible states: “ $Sk_j=1$ ” if the switch is closed or “ $Sk_j=0$ ” if the switch is

open. The nine matrix converter switches can be represented as a 3*3 matrix(7)

$$S = \begin{bmatrix} S_{11} & S_{12} & S_{13} \\ S_{21} & S_{22} & S_{23} \\ S_{31} & S_{32} & S_{33} \end{bmatrix} \quad (7)$$

The relationship between load and input voltages can be expressed as:

$$[v_A v_B v_C]^T = S[v_a v_b v_c]^T \quad (8)$$

The input phase currents can be related to the output phase currents (9), using the transpose of matrix S

$$[i_a i_b i_c]^T = S^T[i_A i_B i_C]^T \quad (9)$$

From the 27 possible switching patterns, time-variant vectors can be obtained (Table:I) representing the matrix output voltages and input currents in $\alpha\beta$ frame [fig 4(b)].

The active and reactive power DPC-MC will select one of these 27 vectors at any given time instant.

Switching Combinations and output voltage/Input Current state-Space Vectors (Table I)

Group	Name	A	B	C	v_{AB}	v_{BC}	v_{CA}	i_a	i_b	i_c	V_o	δ_o	I_i	μ_i
I	1g	a	b	c	v_{ab}	v_{bc}	v_{ca}	i_A	i_B	i_C	v_i	δ_i	$\sqrt{3}i_o$	μ_o
	2g	a	c	b	$-v_{ca}$	$-v_{bc}$	$-v_{ab}$	i_A	i_C	i_B	$-v_i$	$-\delta_i + 4\pi/3$	$\sqrt{3}i_o$	$-\mu_o$
	3g	b	a	c	$-v_{ab}$	$-v_{ca}$	$-v_{bc}$	i_B	i_A	i_C	$-v_i$	δ_i	$\sqrt{3}i_o$	$-\mu_o + 2\pi/3$
	4g	b	c	a	v_{bc}	v_{ca}	v_{ab}	i_C	i_A	i_B	v_i	$\delta_i + 4\pi/3$	$\sqrt{3}i_o$	$\mu_o + 2\pi/3$
	5g	c	a	b	v_{ca}	v_{ab}	v_{bc}	i_B	i_C	i_A	v_i	$\delta_i + 2\pi/3$	$\sqrt{3}i_o$	$\mu_o + 4\pi/3$
	6g	c	b	a	$-v_{bc}$	$-v_{ab}$	$-v_{ca}$	i_C	i_B	i_A	$-v_i$	$-\delta_i + 2\pi/3$	$\sqrt{3}i_o$	$-\mu_o + 4\pi/3$
II	+1	a	b	b	v_{ab}	0	$-v_{ab}$	i_A	$-i_A$	0	$\sqrt{2/3}v_{ab}$	0	$\sqrt{2}i_A$	$-\pi/6$
	-1	b	a	a	$-v_{ab}$	0	v_{ab}	$-i_A$	i_A	0	$-\sqrt{2/3}v_{ab}$	0	$-\sqrt{2}i_A$	$-\pi/6$
	+2	b	c	c	v_{bc}	0	$-v_{bc}$	0	i_A	$-i_A$	$\sqrt{2/3}v_{bc}$	0	$\sqrt{2}i_A$	$\pi/2$
	-2	c	b	b	$-v_{bc}$	0	v_{bc}	0	$-i_A$	i_A	$-\sqrt{2/3}v_{bc}$	0	$-\sqrt{2}i_A$	$\pi/2$
	+3	c	a	a	v_{ca}	0	$-v_{ca}$	$-i_A$	0	i_A	$\sqrt{2/3}v_{ca}$	0	$\sqrt{2}i_A$	$7\pi/6$
	-3	a	c	c	$-v_{ca}$	0	v_{ca}	i_A	0	$-i_A$	$-\sqrt{2/3}v_{ca}$	0	$-\sqrt{2}i_A$	$7\pi/6$
	+4	b	a	b	$-v_{ab}$	v_{ab}	0	i_B	$-i_B$	0	$\sqrt{2/3}v_{ab}$	$2\pi/3$	$\sqrt{2}i_B$	$-\pi/6$
	-4	a	b	a	v_{ab}	$-v_{ab}$	0	$-i_B$	i_B	0	$-\sqrt{2/3}v_{ab}$	$2\pi/3$	$-\sqrt{2}i_B$	$-\pi/6$
	+5	c	b	c	$-v_{bc}$	v_{bc}	0	0	i_B	$-i_B$	$\sqrt{2/3}v_{bc}$	$2\pi/3$	$\sqrt{2}i_B$	$\pi/2$
	-5	b	c	b	v_{bc}	$-v_{bc}$	0	0	$-i_B$	i_B	$-\sqrt{2/3}v_{bc}$	$2\pi/3$	$-\sqrt{2}i_B$	$\pi/2$
	+6	a	c	a	$-v_{ca}$	v_{ca}	0	$-i_B$	0	i_B	$\sqrt{2/3}v_{ca}$	$2\pi/3$	$\sqrt{2}i_B$	$7\pi/6$
	-6	c	a	c	v_{ca}	$-v_{ca}$	0	i_B	0	$-i_B$	$-\sqrt{2/3}v_{ca}$	$2\pi/3$	$-\sqrt{2}i_B$	$7\pi/6$
+7	b	b	a	0	$-v_{ab}$	v_{ab}	i_C	$-i_C$	0	$\sqrt{2/3}v_{ab}$	$4\pi/3$	$\sqrt{2}i_C$	$-\pi/6$	
-7	a	a	b	0	v_{ab}	$-v_{ab}$	$-i_C$	i_C	0	$-\sqrt{2/3}v_{ab}$	$4\pi/3$	$-\sqrt{2}i_C$	$-\pi/6$	
+8	c	c	b	0	$-v_{bc}$	v_{bc}	0	i_C	$-i_C$	$\sqrt{2/3}v_{bc}$	$4\pi/3$	$\sqrt{2}i_C$	$\pi/2$	
-8	b	b	c	0	v_{bc}	$-v_{bc}$	0	$-i_C$	i_C	$-\sqrt{2/3}v_{bc}$	$4\pi/3$	$-\sqrt{2}i_C$	$\pi/2$	
+9	a	a	c	0	$-v_{ca}$	v_{ca}	$-i_C$	0	i_C	$\sqrt{2/3}v_{ca}$	$4\pi/3$	$\sqrt{2}i_C$	$7\pi/6$	
-9	c	c	a	0	v_{ca}	$-v_{ca}$	i_C	0	$-i_C$	$-\sqrt{2/3}v_{ca}$	$4\pi/3$	$-\sqrt{2}i_C$	$7\pi/6$	
III	z_a	a	a	a	0	0	0	0	0	0	0	-	0	-
	z_b	b	b	b	0	0	0	0	0	0	0	-	0	-
	z_c	c	c	c	0	0	0	0	0	0	0	-	0	-

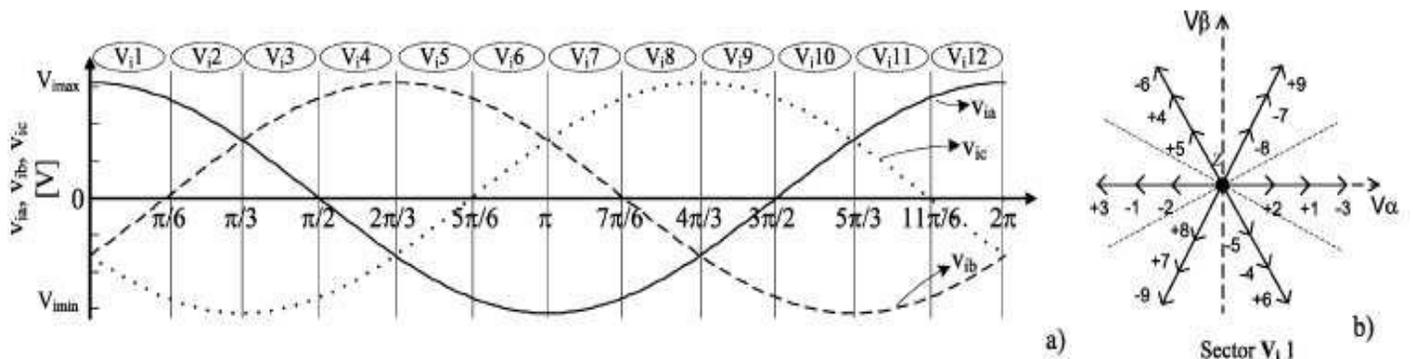


Fig4: (a)Input voltages and their corresponding sector.(b)Output voltage state Space vectors when the input voltages a relocated at sector V_{i1}

3. SPACE VECTOR CONTROL OF MC-UPFC

A. Line Active and Reactive Power Sliding Surfaces

From Fig.2, in steady state, V_d is imposed by source V_s . From (1) and (2), the transmission-line current can be considered as state variables with first-order dynamics dependent on the sources and time constant of impedance L_2/R_2 . Therefore, transmission-line active and reactive powers present first-order dynamics and have a strong relative degree of one, since from the control view point, its first time derivative already contains the control variable from the sliding mode control theory, robust sliding surfaces to control the P and Q variables with a relatively strong degree of one can be obtained, considering proportionality to a linear combination of the errors of the state variables.

Therefore define the active power error e_P and the reactive power error e_Q as the difference between the power references P_{ref}, Q_{ref} and the actual transmitted powers P, Q respectively

$$e_P = P_{ref} - P \quad (10)$$

$$e_Q = Q_{ref} - Q \quad (11)$$

The robust sliding surfaces $SP(e_P, t)$, and $SQ(e_Q, t)$, must be proportional to these errors, being zero after reaching sliding mode

$$SP(e_P, t) = k_P(P_{ref} - P) = 0 \quad (12)$$

$$SQ(e_Q, t) = k_Q(Q_{ref} - Q) = 0 \quad (13)$$

The proportionality gains k_P and k_Q are chosen to impose appropriate switching frequencies.

B. Line Active and Reactive Power Direct Switching

Laws

Based on errors e_P and e_Q to select in real time the matrix converter switching states.

To guarantee stability for active power and reactive power controllers the sliding mode stability conditions (14) and (15) must be verified.

$$SP(e_P, t) \dot{SP}(e_P, t) < 0 \quad (14)$$

$$SQ(e_Q, t) \dot{SQ}(e_Q, t) < 0 \quad (15)$$

a) To choose a vector from (4) and (12), and considering P_{ref} and V_d in steady state, the following can be written:

$$\dot{SP}(e_P, t) = k_P \left(\frac{dP_{ref}}{dt} - \frac{dP}{dt} \right) = -k_P \frac{dP}{dt}$$

$$= -k_P V_d \frac{dI_d}{dt} \quad (16)$$

Considering V_d and P_{ref} as constant if $SP(e_P, t) > 0$, then it must be $\dot{SP}(e_P, t) < 0$

from (16) if $k_P V_d$ is positive then $dI_d/dt > 0$, meaning that P must increase.

from the equivalent model in dq coordinates in (1) if the chosen vector has $V_{Ld} > V_{Rod} - \omega I_q L_2 + R_2 I_d$ then $dI_d/dt > 0$, the selected vector being suitable to increase the active power.
b) from (5) and (13) with reactive power Q_{ref} and V_d in steady state

$$\begin{aligned} \dot{SQ}(e_Q, t) &= k_Q \left(\frac{dQ_{ref}}{dt} - \frac{dQ}{dt} \right) = -k_Q \frac{dQ}{dt} \\ &= k_Q V_d \frac{dI_q}{dt} \end{aligned} \quad (17)$$

if $SQ(e_Q, t) > 0$, then $\dot{SQ}(e_Q, t) < 0$ which still implies $dQ/dt > 0$, meaning that Q must increase. from (17) dI_q/dt must be negative from the equivalent model in dq coordinates in (2) to ensure the reaching condition, the chosen vector has $V_{Lq} < V_{Roq} + \omega I_d L_2 + R_2 I_q$, means that voltage vector has q component suitable to increase the reactive power.

The criteria to choose the matrix vector should be:

- 1) $SP(e_P, t) > 0 \Rightarrow \dot{SP}(e_P, t) < 0 \Rightarrow P < P_{ref}$
Then choose a vector suitable to increase P.
- 2) $SP(e_P, t) < 0 \Rightarrow \dot{SP}(e_P, t) > 0 \Rightarrow P > P_{ref}$
Then choose a vector suitable to decrease P.
3. If $SP(e_P, t) = 0$, (18)

Then choose a vector which does not significantly change the active power.

The same can be applied to reactive power error.

c) control of matrix converter input reactive power

matrix converter UPFC can control the reactive power at its input. voltage source input filter (Fig 3) has a relative degree of two then suitable sliding surface $SQ_i(e_{Q_i}, t)$ will be:

$$SQ_i(e_{Q_i}, t) = (Q_{iref} - Q_i) + K_{Q_i} \frac{d}{dt} (Q_{iref} - Q_i) \quad (19)$$

K_{Q_i} is chosen to obtain a suitable switching frequency.

By considering (6) in above equation i_q , matrix input current must have enough amplitude to fulfil stability.

1. $SQ_i(e_{Q_i}, t) > 0 \Rightarrow \dot{SQ}_i(e_{Q_i}, t) < 0$, then select vector with current $i_q < 0$ to increase Q_i

2. $SQ_i(e_{Q_i}, t) < 0 \Rightarrow \dot{SQ}_i(e_{Q_i}, t) > 0$, then

Select vector with current $i_q > 0$ to decrease Q_i .

The sign of matrix converter reactive power Q_i is determined by knowing the location of the input voltages and the location

of the output currents in Fig:5(input current depends on output currents from TABLE I).

To ease the vector selection Sliding surfaces $SP(eP,t)$ and $SQ(eQ,t)$ should be transformed to $\alpha\beta$ coordinates $S\alpha(eP,t)$ and $S\beta(eQ,t)$.

If the control errors eP and eQ are quantised using two hysteresis comparators each with three levels $(-1,0,1)$ nine output voltage vector error combinations are obtained .if a two level comparator is used to control the shunt reactive power ,18 error combinations will be defined ,enabling the selection of 18 vectors.

The sliding surfaces for input reactive power is quantised in two levels $(-1$ and $+1)$ using one hysteresis comparator.

applied should be $+9$ or -7 Using the same reasoning for the remaining eight active and reactive power error combinations and generalize it for all other input voltage sectors ,TABLE II is obtained.

The final choice between $+9$ and -7 depends on the matrix reactive power controller CQ_i ,However, at sector I_{o1} ,if i_q has a suitable amplitude, vector $+9$ leads to $\dot{S}Q_i(eQ,t) > 0$ while vector -7 originates $\dot{S}Q_i(eQ,t) < 0$.So, vector $+9$ is chosen if input reactive power sliding surface $SQ_i(eQ_i,t)$ is quantised as $CQ_i = -1$,while

vector -7 is chosen when $SQ_i(eQ_i,t)$ is quantised as $CQ_i = 1$ Using same for remaining eight combination at sector V_{i1} and applying it for the other output current sectors, TABLE III is obtained shown below:

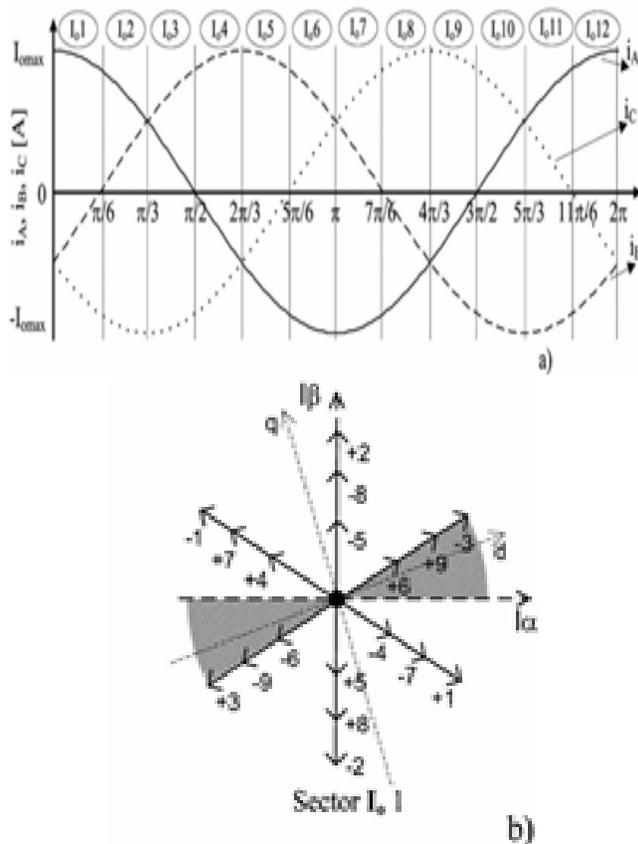


Fig.5: (a)Output currents and their corresponding sector.(b)Input current state-space vectors, when output currents are relocated at sector I_{o1} .The dq axis is represented, considering that the input voltages are located in zone V_{i1} .

As an example consider the case of $C\alpha = S\alpha(eP,t) > 0$ and $C\beta = S\beta(eQ,t) < 0$. Then, $dP/dt > 0$ and $dQ/dt < 0$ imply that $dI_\alpha/dt > 0$ and $dI_\beta/dt > 0$.To choose the adequate output voltage vector it is necessary to know the input voltage location suppose ,it is in V_{i1} [fig 4(b)] then vector to be

TABLE-II STATE SPACE VECTORS SELECTION FOR DIFFERENT ERROR COMBINATIONS

C_α	C_β	Sector					
		$V_i 12; 1$	$V_i 2; 3$	$V_i 4; 5$	$V_i 6; 7$	$V_i 8; 9$	$V_i 10; 11$
-1	+1	-9; +7	-9; +8	+8; -7	-7; +9	+9; -8	-8; +7
-1	0	+3; -1	+3; -2	-2; +1	+1; -3	-3; +2	+2; -1
-1	-1	-6; +4	-6; +5	+5; -4	-4; +6	+6; -5	-5; +4
0	+1	-9; +7; +6; -4	-9; +8; +6; -5	+8; -7; -5; +4	-7; +9; +4; -6	+9; -8; -6; +5	-8; +7; +5; -4
0	0	Za; Zb; Zc; -8;+2;-5;+8;-2;+5	Za; Zb; Zc; -7;+1;-4; +7;-1;+4	Za; Zb; Zc; +9;-3;+6;-9;+3;-6	Za; Zb; Zc; -8;+2;-5;+8;-2;+5	Za; Zb; Zc; -7;+1;-4; +7;-1;+4	Za; Zb; Zc; -9;+3;-6; +9;-3;+6
0	-1	-6; +4; +9; -7	+5; -6; -8; +9	+5; -4; -8; +7	-4; +6; +7; -9	+6; -5; -9; +8	-5; +4; +8; -7
+1	+1	+6; -4	+6; -5	-5; +4	+4; -6	-6; +5	+5; -4
+1	0	-3; +1	+2; -3	-1; +2	+3; -1	-2; +3	+1; -2
+1	-1	+9; -7	+9; -8	+7; -8	+7; -9	-9; +8	+8; -7

TABLE III STATE SPACE VECTORS SELECTION, FOR INPUT VOLTAGES LOCATED AT DIFFERENT VOLTAGE SECTORS

$C_\alpha C_\beta$	$I_{o12}; I_{o1}$	$I_{o2}; I_{o3}$	$I_{o4}; I_{o5}$	$I_{o6}; I_{o7}$	$I_{o8}; I_{o9}$	$I_{o10}; I_{o11}$
V_{i1}	CQi	CQi	CQi	CQi	CQi	CQi
	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1
	-1 +1	-9 +7	-9 +7	-9 +7	+7 -9	+7 -9
-1 0	+3 -1	+3 -1	-1 +3	-1 +3	-1 +3	+3 -1
-1 -1	-6 +4	+4 -6	+4 -6	+4 -6	-6 +4	-6 +4
0 +1	-9 +7	-9 +7	-9 +7	+7 -9	+7 -9	+7 -9
0 0	-2 +2	+8 -8	-5 +5	+2 -2	-8+8	+5 -5
0 -1	-7 +9	-7 +9	-7 +9	+9-7	+9-7	+9-7
+1 +1	-4 +6	+6-4	+6-4	+6-4	-4 +6	-4 +6
+1 0	+1 -3	+1 -3	-3 +1	-3 +1	-3 +1	+1 -3
+1 -1	-7 +9	-7 +9	-7 +9	+9-7	+9-7	+9-7
$C_\alpha C_\beta$	$I_{o12}; I_{o1}$	$I_{o2}; I_{o3}$	$I_{o4}; I_{o5}$	$I_{o6}; I_{o7}$	$I_{o8}; I_{o9}$	$I_{o10}; I_{o11}$
V_{i2}	CQi	CQi	CQi	CQi	CQi	CQi
	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1
	-1 +1	-9+8	-9 +8	-9 +8	+8-9	+8-9
-1 0	+3-2	+3-2	-2 +3	-2 +3	-2 +3	+3-2
-1 -1	-6+5	+5-6	+5-6	+5-6	-6+5	-6+5
0 +1	-9 +8	-9+8	-9 +8	+8 -9	+8 -9	+8 -9
0 0	-1+1	+7 -7	-4 +4	+1 -1	-7 +7	+4-4
0 -1	+9-8	+9-8	+9-8	-8+9	-8+9	-8+9
+1 +1	-5 +6	+6-5	+6-5	+6-5	-5+6	-5+6
+1 0	-3+2	-3+2	+2 -3	+2 -3	-3 -3	-3+2
+1 -1	-8+9	-8+9	-8+9	+9-8	+9-8	+9-8

C α C β Vi3	Io12;Io1	Io2;Io3	Io4;Io5	Io6;Io7	Io8;Io9	Io10; Io11
	CQi	CQi	CQi	CQi	CQi	CQi
	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1
-1 +1	+8 -7	+8 -7	+8 -7	-7 +8	-7 +8	-7 +8
-1 0	-2 +1	-2 +1	+1-2	+1-2	+1-2	-2 +1
-1 -1	+5 -4	-4 +5	-4 +5	-4 +5	+5 -4	+5 -4
0 +1	+8 -7	+8 -7	+8 -7	-7+8	-7+8	-7 +8
0 0	+3 -3	-9 +9	+6 -6	-3+3	+9 -9	-6 +6
0 -1	+7 -8	+7 -8	+7-8	-8 +7	-8 +7	-8 +7
+1 +1	+4 -5	-5 +4	-5 +4	-5 +4	+4 -5	+4 -5
+1 0	+2 -1	+2 -1	-1 +2	-1 +2	-1 +2	+2 -1
+1 -1	-8 +7	-8 +7	-8 +7	+7 -8	+7 -8	+7 -8

C α C β Vi4	Io12;Io1	Io2;Io3	Io4;Io5	Io6;Io7	Io8;Io9	Io10;Io11
	CQi	CQi	CQi	CQi	CQi	CQi
	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1
-1 +1	-7 9	-7 9	-7 +9	+9-7	9-7	9-7
-1 0	+1 -3	+1 -3	-3+1	-3+1	-3+1	+1 -3
-1 -1	-4 +6	+6 -4	+6 -4	+6 -4	-4 +6	-4 +6
0 +1	-7 +9	-7 +9	-7 +9	+9-7	+9-7	+9-7
0 0	-2 +2	+8 -8	-5 +5	-2 +2	-8+8	+5 -5
0 -1	-9 +7	-9 +7	-9 +7	+7 -9	+7 -9	+7 -9
+1 +1	-6 +4	+4 -6	+4 -6	+4 -6	-6 +4	-6 +4
+1 0	-1 +3	-1 +3	+3 -1	+3 -1	+3 -1	-1 +3
+1 -1	-9 +7	-9 +7	-9 +7	+7 -9	+7 -9	+7 -9

C α C β Vi5	Io12;Io1	Io2;Io3	Io4;Io5	Io6;Io7	Io8;Io9	Io10;Io11
	CQi	CQi	CQi	CQi	CQi	CQi
	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1
-1 +1	+9-8	+9-8	+9-8	-8+9	-8+9	-8+9
-1 0	-3+2	-3+2	+2 -3	+2 -3	+2 -3	-3+2
-1 -1	+6-5	-5+6	-5+6	-5+6	+6-5	+6-5
0 +1	+9-8	+9-8	+9-8	-8+9	-8+9	-8+9
0 0	-1 +1	+7 -7	-4 +4	+1 -1	-7 +7	+4-4
0 -1	+8-9	+8-9	+8-9	-9 +8	-9 +8	-9 +8
+1 +1	+5-6	-6+5	-6+5	-6+5	+5-6	+5-6
+1 0	+3-2	+3-2	-2 +3	-2 +3	-2 +3	+3-2
+1 -1	+8-9	+8-9	+8-9	-9 +8	-9 +8	-9 +8

C α C β Vi6	Io12;Io1	Io2;Io3	Io4;Io5	Io6;Io7	Io8;Io9	Io10;Io11
	CQi	CQi	CQi	CQi	CQi	CQi
	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1	+1 -1
-1 +1	-8 +7	-8 +7	-8 +7	+7 -8	+7 -8	+7 -8
-1 0	+2 -1	+2 -1	-1 +2	-1 +2	-1 +2	+2 -1
-1 -1	-5 +4	+4 -5	+4 -5	+4 -5	-5 +4	-5 +4
0 +1	-8 +7	-8 +7	-8 +7	+7 -8	+7 -8	+7 -8
0 0	+3 -3	-9 +9	+6 -6	-3+3	+9 -9	-6+6
0 -1	-7 +8	-7 +8	-7 +8	+8 -7	+8 -7	+8 -7
+1 +1	-4 +5	+5-4	+5-4	+5-4	-4 +5	-4 +5
+1 0	-2 +1	-2 +1	+1-2	+1-2	+1-2	-2 +1
+1 -1	-7+8	-7+8	-7+8	+8 -7	+8 -7	+8 -7

4.

IMPLEMENTATION OF THE MCAS UPFC:

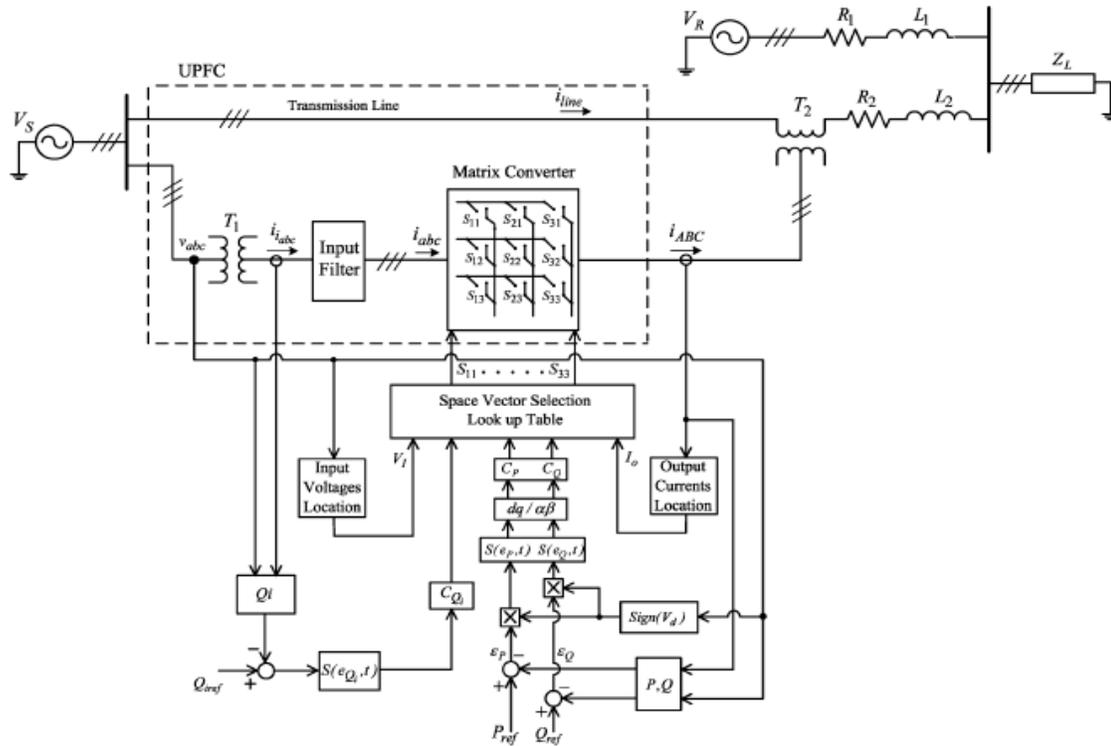
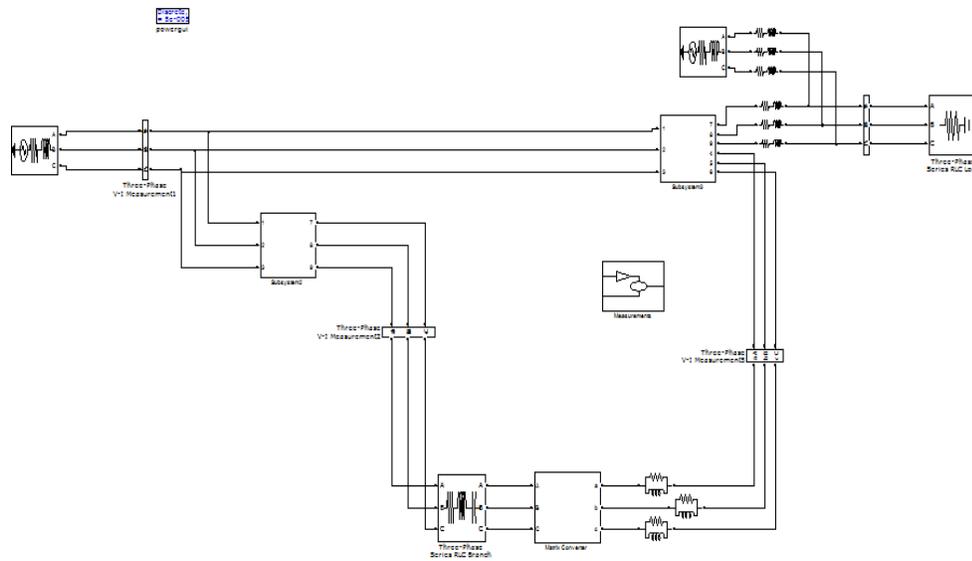
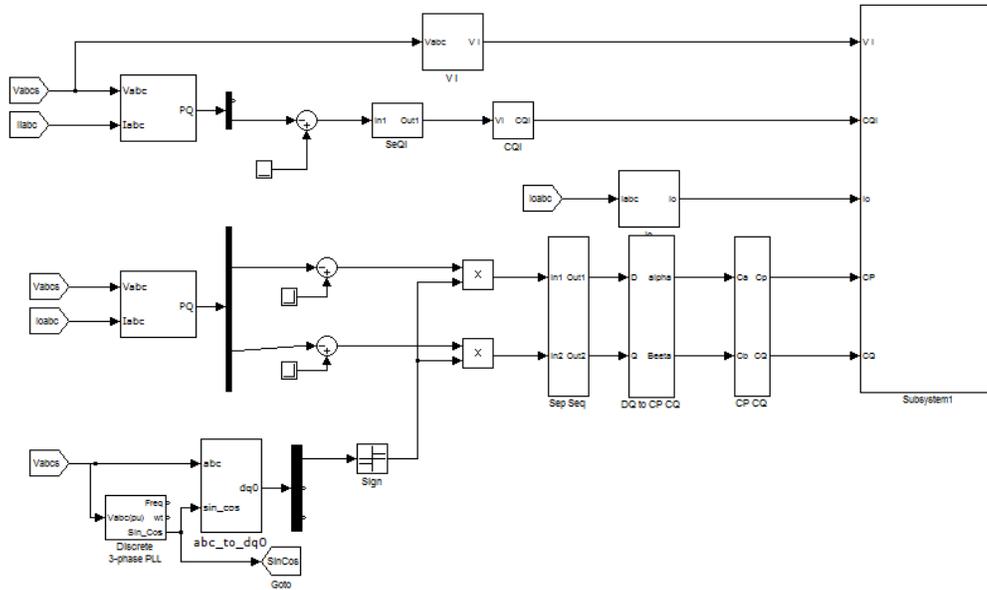


Fig 6 Control scheme of DPC of the three phase matrix converter operating as the UPFC

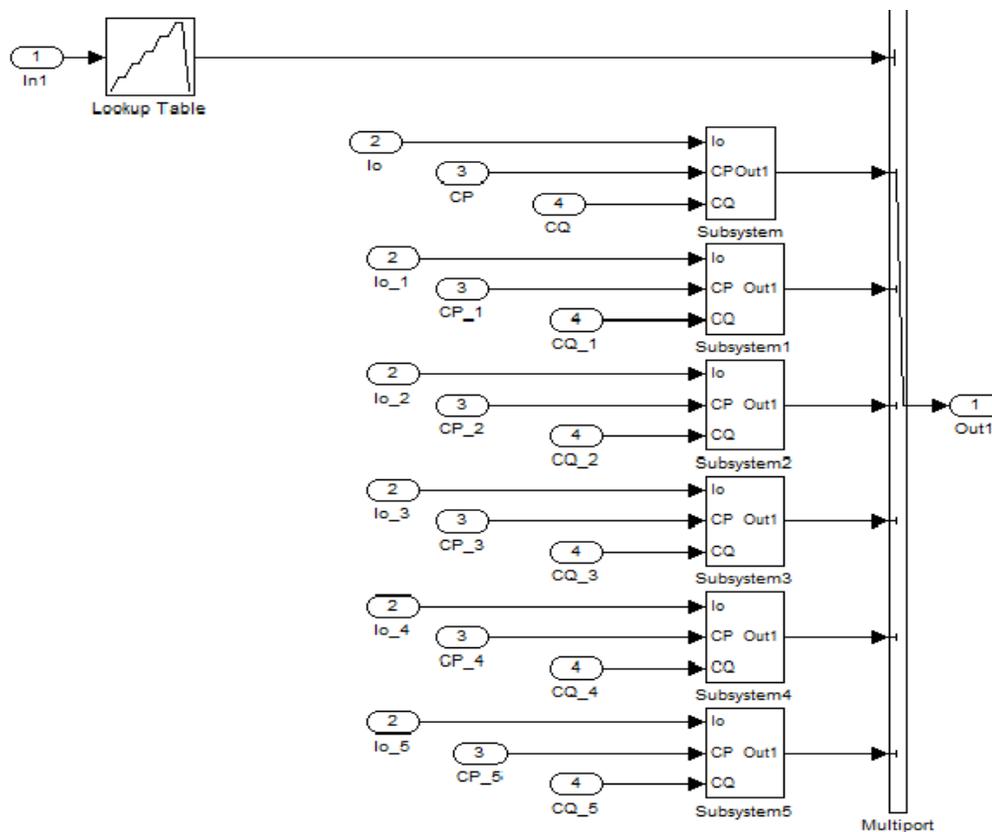
5. SIMULATION AND EXPERIMENTAL RESULT



Transmission network with matrix converter - UPFC



Control scheme of matrix converter as UPFC using space vector selection table



State space vector selection

Fig.7(a)and(b)shows ,respectively, the simulation results of the active and reactive power step response($\Delta P_{ref}=+0.4p.u.$ and $\Delta Q_{ref}=+0.2p.u.$) and shunt reactive power, considering initial reference values: $P_{ref}=0.4p.u.$, $Q_{ref}=-0.2p.u.$,and $Q_{ref}=-0.07p.u.$.Bothresults clearly show that there is no cross-coupling between active and reactive power.

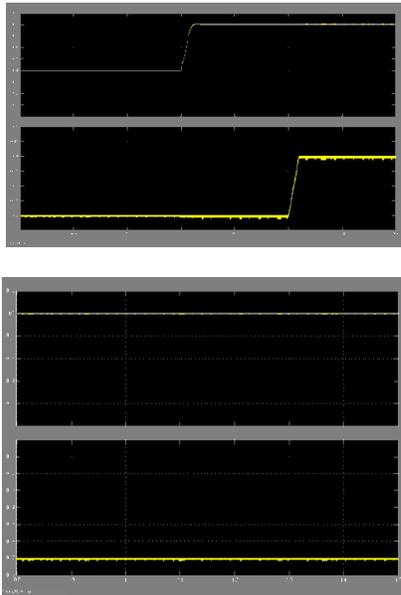


Fig7(a),(b):Active and reactive series power response and reactive shunt power, for P and Q steps

The result of Fig.8(a)and(b) show line and input matrix converter currents in steady state, for $P_{ref}=0.4 p.u.$, $Q_{ref}=0.2p.u.$,and $Q_{ref}=-0.07p.u.$.Currents are almost sinusoidal with small ripple content.

DPC simulation results presented in Fig.9(a)and(b), showing the claimed DPC faster dynamic response to step active and reactive power reference change.

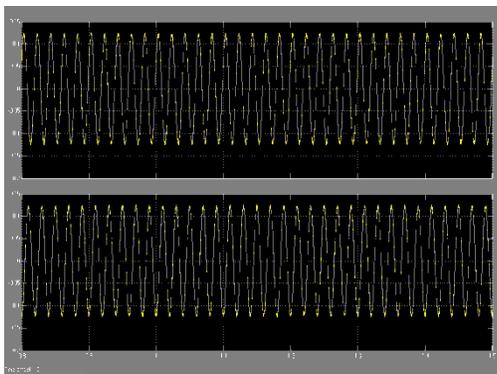


Fig.8(a),(b): Line currents (i_A, i_B) and input matrix converter currents (i_b, i_c) pu. Simulation results

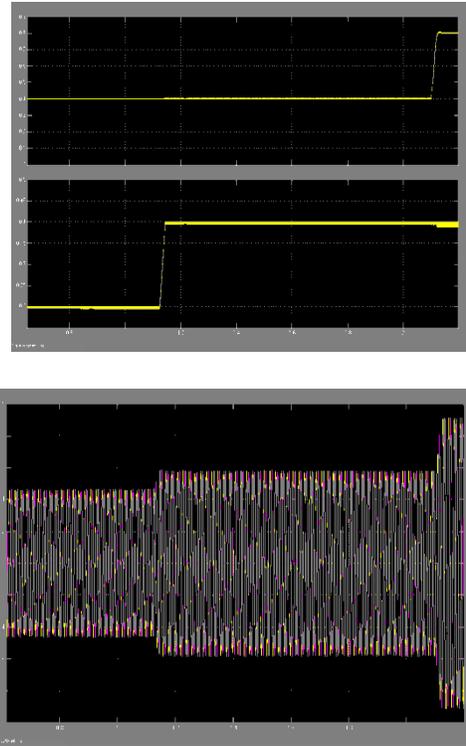
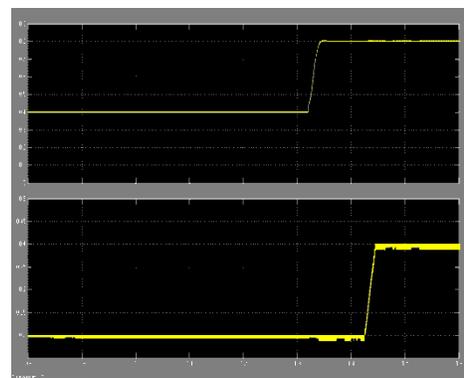


Fig9(a),(b): Active and reactive power response and line currents for a P and Q step change Direct power controller simulations

DPC controller ability to operate at lower switching frequencies, the DPC gains were lowered and the input filter parameters were changed accordingly ($r=25\Omega$, $l=5.9m$ HC= $12.6\mu F$) to lower the switching frequency to nearly 1.4kHz. The results (Fig.10) also show fast response without cross coupling between active and reactive power. This confirms the DPC-MC robustness to input filter parameter variation, the ability to operate at low switching frequencies, and insensitivity to Switching nonlinearity.



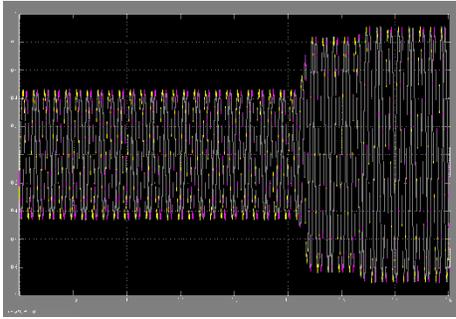


Fig10 (a)(b): Active and reactive power response and line currents for a P and Q

CONCLUSIONS

This paper derived advanced non linear direct power controllers for matrix converters connected to power transmission lines as UPFCs. Presented simulation & experimental results show that active and reactive flow will be advantageously controlled by using the proposed DPC. Results show no steady-state errors, no cross coupling, insensitivity to non modelled dynamics and fast response times, thus confirming the expected performance of the presented nonlinear DPC methodology. Despite showing a suitable dynamic response, the PI performance is inferior when compared to DPC. Furthermore, the PI controllers and modulator take longer times to compute. Obtained results show that DPC is a strong non linear control candidate for line active and reactive power flow.

REFERENCES

- [1] N. Hingorani and L. Gyugyi, *Understanding FACTS—Concepts and Technology of Flexible AC Transmission Systems*. Piscataway, NJ:IEEE Press/Wiley, 2000.
- [2] L. Gyugyi, “Unified power flow control concept for flexible AC transmission systems,” *Proc. Inst. Elect. Eng. C*, vol. 139, no. 4, Jul. 1992.
- [3] L. Gyugyi, C. Schauder, S. Williams, T. Rietman, D. Torgerson, and A. Edris, “The unified power flow controller: A new approach to power transmission control,” *IEEE Trans. Power Del.*, vol. 10, no. 2, pp. 1085–1097, Apr. 1995.
- [4] C. Schauder, L. Gyugyi, M. Lund, D. Hamai, T. Rietman, D. Torgerson, and A. Edris, “Operation of the unified power flow controller (UPFC) under practical constraints,” *IEEE Trans. Power Del.*, vol. 13, no. 2, pp. 630–639, Apr. 1998.
- [5] T. Ma, “P-Q decoupled control schemes using fuzzy neural networks for the unified power flow controller,” in *Electr. Power Energy Syst.*. New York: Elsevier, Dec. 2007, vol. 29, pp. 748–748.
- [6] L. Liu, P. Zhu, Y. Kang, and J. Chen, “Power-flow control performance analysis of a unified power-flow controller in a novel control scheme,” *IEEE Trans. Power Del.*, vol. 22, no. 3, pp. 1613–1619, Jul. 2007.
- [7] F. Gao and M. Iravani, “Dynamic model of a space vector modulated matrix converter,” *IEEE Trans. Power Del.*, vol. 22, no. 3, pp. 1696–1750, Jul. 2007.
- [8] B. Geethalakshmi and P. Dananjayan, “Investigation of performance of UPFC without DC link capacitor,” in *Elect. Power Energy Res.*. New York: Elsevier, 2008, pp. 284–294, 736-746.
- [9] X. Jiang, X. Fang, J. Chow, A. Edris, E. Uzunovic, M. Parisi, and L. Hopkins, “A novel approach for modeling voltage-sourced converter based FACTS controllers,” *IEEE Trans. Power Del.*, vol. 23, no. 4, pp. 2591–2598, Oct. 2008.
- [10] R. Strzelecki, A. Noculak, H. Tunia, and K. Sozanski, “UPFC with matrix converter,” presented at the EPE Conf., Graz, Austria, Sep. 2001.
- [11] J. Monteiro, J. Silva, S. Pinto, and J. Palma, “Unified power flow controllers without DC bus: Designing controllers for the matrix converter solution,” presented at the Int. Conf. Electrical Engineering, Coimbra, Portugal, 2005.
- [12] A. Dasgupta, P. Tripathy, and P. Sensarma, “Matrix converter as UPFC for transmission line compensation,” in *Proc. 7th Int. Conf. Power Electronics*, Exco, Daegu, Korea, Oct. 2007, pp. 1050–1055.
- [13] P. Wheeler, J. Rodriguez, J. Clare, L. Empringham, and A. Weinstein, “Matrix converters: A technology review,” *IEEE Trans. Ind. Electron.*, vol. 49, no. 2, pp. 276–288, Apr. 2002.
- [14] S. Pinto, “*Conversores matriciais trifásicos: generalização do comando vectorial directo*,” Ph.D. dissertation, Instituto Superior Técnico Universidade Técnica de Lisboa, Lisbon, Portugal, Jul. 2003.
- [15] T. Podlesak, D. Katsis, P. Wheeler, J. Clare, L. Empringham, and M. Bland, “A 150-kVA vector-controlled matrix converter induction motor drive,” *IEEE Trans. Ind. Appl.*, vol. 41, no. 3, pp. 841–847, May/Jun. 2005.
- [16] R. Cárdenas, R. Pena, P. Wheeler, J. Clare, and G. Asher, “Control of the reactive power supplied by WECS based on an induction generator fed by a matrix converter,” *IEEE Trans. Ind. Electron.*, vol. 56, no. 2, pp. 429–438, Feb. 2009.
- [17] A. Alesina and M. G. B. Venturini, “Solid-state power conversion: A Fourier analysis approach to generalized transformer synthesis,” *IEEE Trans. Circuits Syst.*, vol. CAS-28, no. 4, pp. 319–330, Apr. 1981.
- [18] P. J. Wheeler, J. Clare, and L. Empringham, “Enhancement of matrix converter output waveform quality using minimized commutation times,” *IEEE Trans. Ind. Electron.*, vol. 51, no. 1, pp. 240–244, Feb. 2004.
- [19] H. Akagi, Y. Kanazawa, and A. Nabae, “Instantaneous reactive power compensators comprising switching devices without energy storage components,” *IEEE Trans. Ind. Appl.*, vol. IA-20, no. 3, pp. 625–630, May/Jun. 1984.
- [20] I. Martins, J. Barros, and J. Silva, “Design of cross-coupling free current mode controller for UPFC series converter,” in *Proc. IEEE Optimization of Electrical and Electronic Equipment Conf.*, May 2008, pp. 209–218.