MODIFIED DC-DC CONVERTER FOR HIGH INPUT VOLTAGE APPLICATIONS

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Abstract:
This paper proposes a model for high input voltage applications using two fly-back converters. In this the components are arranged by using integrated magnetic techniques. To reduce the voltage stresses the input components are connected in series and to obtain the required output the output components are connected in parallel. The three cores of the fly-back transformers are air gaped to reduce the current spikes which are caused due to coupling and the secondary side of the transformers are inversely coupled for ripple cancellation. This is done by using a prototype which helps us to obtain output as we desired for certain input with certain transformation ratio. The components used are IGBT’s, capacitors and fly-back transformers.

Keywords: Integrated magnetic techniques fly back converter, coupling, ripple reduction, interleaved operation.

I. INTRODUCTION:
Integrated magnetic techniques combine transformers and inductors into a single core in order to reduce the size and cost of magnetic components in dc/dc converters. Principles of modeling and designing integrated magnetic have been widely published. Historically, integrated dc/dc converters were first developed for Forward converters. However, recently integrated magnetic full wave circuits have been extensively studied. These full wave circuits have reported significant benefits, including high efficiency, high power density and low switch stress. Applications of these circuits are being proposed in 30 W - 100 W ranges, and have great potential for telecommunication power supplies. A “typical”\textsuperscript{2} integrated magnetic full wave dc/dc converter, which uses a three-leg magnetic core. This class of full wave integrated magnetic circuit’s exhibits a common character that there is no winding on the inductor leg. Instead, the secondary windings play the role of the equivalent Inductor winding, and hence, the secondary windings must satisfy both power transfer and current ripple restrictions for different operating stages. The elimination of the inductor winding comes from the magnetic integration methods, and, in applications, this is believed to reduce the copper loss and simplify the transformer structure. However, optimum transformer design must keep the balance between copper loss, ferrite loss and total size. In integrated magnetic transformers, the number of secondary winding turns influences the number of primary winding turns, the magnetic core size and the output current characteristics. This places a burden on the secondary winding turn decision and complicates the transformer design. As a result, the magnetic core often needs to be specially designed and manufactured in order to satisfy both output current ripple requirements and flux density constraints. Using flyback converter this integrated magnetic techniques are employed. It is equivalent to that of a buck-boost converter, with the inductor split to form a transformer. Therefore the operating principle of both converters is very close:

When the switch is on, the primary of the transformer is directly connected to the input voltage source. This results in an increase of magnetic flux in the transformer. The voltage across the secondary winding is negative, so the diode is reverse-biased (i.e., blocked). The output capacitor supplies energy to the output load. When the switch is off, the energy stored in the transformer is transferred to the output of the converter. The operation of storing energy in the transformer before transferring to the output of the converter allows the topology to easily generate multiple outputs with little additional circuitry, although the output voltages have to be able to match each other through the turns ratio.

This paper uses two fly-back converters which act as fly-back transformers which are designed under a single magnetic core. Earlier they designed a converter based on this concept. The main features of that converter are

- Earlier designed a single magnetic core three limbed transformer with center limb air gapped. So that the current spikes can be reduced.
- The primary and secondary side of the transformer is connected in parallel the operation is similar to that of a fly-back converter with two switches on the primary side and two capacitors on the secondary side. The secondary side is directly coupled.
Whereas coming to the modified converter the changes made and the specified reasons for the changes are as discussed below.

- Connecting the primary side in series reduces the voltage stresses of the primary components. The primary switches are rated at half of the corresponding voltage values in a single Fly-back or a traditional full wave buck-boost power converter.
- The on-resistance and cost of the primary switches are, thus, significantly reduced. Alternatively, this implies that for the same rated switches, the proposed converter can handle twice the input voltage compared with the converter.
- Interleaved operation with paralleled connection on the secondary side keeps the benefits of ripple reduction as a traditional full wave buck-boost power converter does.
- To satisfy a certain current ripple requirement, magnetizing inductance can be reduced by means of suitable inductor coupling design.
- Due to the mutual influence of the two coupled inductors that is combined with the transformers, further current ripple reduction can be achieved when compared with interleaved Fly-back converters without coupling.
- Also, the amount of the magnetic components is reduced by integrating the two transformers into one magnetic core, although the efficiency of the new approach may not be improved when compared with conventional discrete interleaved Fly-backs.
- Current spike caused by the voltage mismatch between the windings can be suppressed by gapping all the center and outer legs. Compared with tight coupling, such as the condition in a traditional full wave buck-boost power converter, gapping all the legs weakens the coupling between the two Fly-backs’ windings, and thus, reduces the effect of the voltage mismatch. Between the two Fly-backs’ windings, and thus, reduces the effect of the voltage mismatch.

The modified converter is as shown below which consists of IGBT’s which act as switches on the primary side, the capacitors help in the timing operation of the converter depending on the intervals. And the secondary consists of diodes D1 and D2 which are reversely coupled for the sake of ripple reduction. And the operation of the converter is as discussed below.

![Figure](image_url)

**Figure** representing the circuit model of the modified dc-dc converter

**Operation principle**

The main primaries switch and are the auxiliary switches, which are connected with the reset windings to obtain active clamp. Vc1, Vc2, Vc3 are the voltages across c1, c2, c3. Capacitors Cr1 and Cr2 are the equivalent resonant capacitors of q1, q2. c1, q1, t1, q3 and c3 are the primary side components of one active-clamp forward, while the other forward consists of c2, q2, t2, q4 and c3. The two forward circuits share the clamping capacitor c3 so that the charge balances of c1 and c2 are kept. Since the control signals of the two channels have the same duty cycle, unbalanced voltage on c1, c2 and can generate different magnetizing current to charge the capacitors. Therefore, the two input voltages are adjusted to a balanced value at steady state. Also, the two channels share the freewheeling switch q7 and output inductor L1 on the secondary side. Therefore, there is no current sharing problem.

The size of the output inductor is reduced due to interleaved operation. One potential concern is the effect of the leakage inductor between the primary winding and the active clamp winding. The energy stored in the leakage inductor can impose excessive voltage spikes on the switches. This leads to higher voltage stress on the switches and should be taken into
account for design. Thus, transformers with tight coupling are required. Also, effective snubber is helpful to reduce the voltage stress. The following description explains the detailed operation principle of the proposed topology.

In general, circuit operation can be qualitatively described over several modes/intervals. The upper forward circuit transfers energy to secondary side during time interval t0 – t1. The under forward circuit transfer energy during interval t7 – t8. Interval t4 – t5 and interval t11 – t12 are utilized for the freewheeling process of the output current. All the remaining intervals are transition intervals and are substantially smaller in length than energy transfer or freewheeling time intervals. Interval t0 – t14 is defined as the switching period T1. The lengths of t0 – t1 and t7 – t8 are defined as DT, which represents the duty ratio, and thus, the duty ratio D will always be less than one-half. The lengths of t1 – t4 and t8 – t11 are set to a suitable value td, which is longer than the required transition time t1 – t3 and t8 – t10. The length of t0 – t1 and t7 – t8 are defined as DT, which represents the duty ratio.

2. FLUX DISTRIBUTIONS ANALYSIS:

For the convenience of magnetic design, it is necessary to provide the formulas for flux distributions. To prevent the magnetic core from saturating, the peak values of the flux densities in all three legs should be kept below a specified maximum value. Therefore, this section will discuss the detailed calculation for both the ac fluxes and dc fluxes in all three legs. As a result, the peak values of the fluxes are the sum of the dc fluxes and half of the ac fluxes in all three legs. As a result, the peak values of the fluxes are the sum of the ac fluxes and half of the dc fluxes shows the magnetic reluctance circuit of the proposed topology, assuming the conditions of the two outer legs are symmetrical. Also, Fig shows the flux path in the core of the transformer. Because of the air gap, the center leg is no longer a low magnetic reluctance path for the fluxes. The flux generated by each winding in the outer legs can go through all three legs. The flux interaction between the windings affects the current triple and peak values of the flux densities. Furthermore, following figure shows the detailed equivalent magnetic reluctance circuit during each interval. The effect of the corresponding winding is removed when the current in the winding is zero.

Thus by the analysis all the fluxes in the system i.e. both the ac and dc fluxes obtained are

Of which fig a represents flux path during the interval t0-t1 similarly fig b during t1-t2, fig c during t2-t3 and fig d during t3-t4. The equations corresponding to their flux paths and intervals are

- T0-T1: \( \Phi_c = (Vin/2Np)-(V0/Ns) \)
- T1-T2: \( \Phi_c = -(2V0/Ns) \)
- T2-T3: \( \Phi_c = (Vin/2Np)-(V0/Ns) \)
- T3-T4: \( \Phi_c = -(2V0/Ns) \)

Figures a,b,c,d respectively shows the flux path in the core.
Total ac flux in the two outer limbs is obtained as

\[ \Delta \phi_1 = \Delta \phi_2 = V_0 (1-D)/N_s f_s \]

The total ac flux in the center limb obtained as

\[ \Delta \phi_c = V_0 (1-2D)/N_s f_s \]

DC Flux:

\[ \Phi_c = \phi_1 + \phi_2 = \frac{(N_{p1}I_{p1} + N_{s1}I_{s1} + N_{p2}I_{p2} + N_{s2}I_{s2})}{(1/(R_0 + 2R_c)} \]

To avoid the saturation of the magnetic core, the peak value of the flux densities of all three legs should be computed. The peak value can be obtained by the sum of DC fluxes and half of the total AC fluxes. By using the calculation of the current ripple and the peak flux densities suitable magnetic core size and the turnnumbers of the windings can be chosen to satisfy the current ripple restrictions and the magnetic constraints.

3. SIMULATION AND RESULTS:

The Simulink model of the converter is as shown below. In this dual converter two interleaved forward circuits are connected in series on the primary side and in parallel on the secondary side the two transformers share one low voltage capacitor for clamping through auxiliary windings and switches. Through the resonance between the magnetizing inductors and equivalent capacitors of the switches during the transition time, switching loss of the primary switches can be reduced. Output current and voltage waveforms when firing pulses from same firing circuit. When firing pulses are giving from a single pulse generating circuit waveforms are shown in graph here input voltage is 400V, output is 20V and current.

![Fig-1: output current and voltage wave forms when firing pulses from same firing circuit.](image)

Specifically, the following differences are remarked when compared with a traditional converter in.

1) The new approach connects the input of the two Fly backs in series, while the traditional method connects the input in parallel.
2) The proposed converter has three gaps in all legs. The traditional converter only has one gap in the center leg.
3) The secondary windings are inversely coupled in the new converter, and directly coupled in the traditional converter.

From the first difference, it can be concluded that the proposed method has half of the voltage stress when compared with the traditional method. According to the second and third differences, the secondary windings of the traditional converter are directly coupled with tight coupling. However, for practical circuits, especially for this high voltage application, switches cannot operate exactly simultaneously. There is a voltage mismatch between the two secondary windings. The voltage difference is applied to the leakage inductance between the two windings. Because the two outer legs have low reluctances, and the center leg has a high reluctance, the two secondary windings have good coupling and small leakage inductance. Minor mismatch can lead to high current spikes and resonance. On the other hand, the secondary windings of the new converter are inversely coupled with not so tight couplings since all three legs are gapped. By gapping all the three legs, a big leakage inductance between the windings of the two Transformers is obtained, since the flux generated by each winding in the outer legs can go through all three legs (is approximately equal to 1/3 when all the legs have gaps with approximately same distances.) The large inductance value can avoid high current spike when a voltage mismatch is applied. Also, current ripple reduction can still be achieved with suitable coupling design. We also remark that the weakened coupling between the two transformers allows the duty ratio to be greater than 50% in the propose circuit.

![Fig-2: output voltage and current wave forms when firing pulses are from different firing circuits.](image)
CONCLUSIONS

The dual interleaved forward converter may be beneficial for high current/ high power density designs. This converter topology would be ideal for intermediate bus converters and merchant power applications because the reduced input and output capacitor ripple current reduces electrical stress on the input and output capacitors. The inductor ripple current cancellation of interleaving converters allows the design more output capacitance, which in turn should reduce the design's output capacitance requirements.

Interleaving techniques are adopted to reduce input/output current or voltage ripples for converters with critical conduction mode. This is useful for not only buck operation and also for boosting purpose. In this study implements two types of phase-shift control schemes for critical interleaved boost PFC converters. Sub-harmonic oscillation phenomena occur at high line voltage when one of them is adopted. So this paper presents a quantitative analysis on the phenomena and proposes a novel phase-shift control scheme.

REFERENCES: